

## SEARCH REQUEST FORM

43

Requestor's Name: ARIO ETIENNE Serial Number: 09/048,009  
Date: 1/6/00 Phone: 308-7562 Art Unit: 2781

### Search Topic:

Please write a detailed statement of search topic. Describe specifically as possible the subject matter to be searched. Define any terms that may have a special meaning. Give examples or relevant citations, authors, keywords, etc., if known. For sequences, please attach a copy of the sequence. You may include a copy of the broadest and/or most relevant claim(s).

BASIC CONCEPT: A USER CARD OR A SMART CARD THAT CAN BE  
SELECTIVELY OPERATED IN MULTIPLE DIFFERENT PROTOCOLS/MODE.  
THE CARD COMPRISES A MICROPROCESSOR/PROCESSOR WHICH  
IS RESPONSIVE TO A MODE/PROTOCOL SIGNAL FOR CAUSING  
WHICH CAUSES THE MICROPROCESSOR AND, THEREFORE, THE CARD  
TO OPERATE IN A FIRST MODE/PROTOCOL. SAID MODE  
SIGNAL IS DETECTED PRESENT, OR, OTHERWISE, CAUSING  
THE MICROPROCESSOR/PROCESSOR TO OPERATE IN A SECOND PROTOCOL.

INVENTOR: JEAN-MARC SARAT

Consider for next  
action by  
following

01-07-00 A08:15 IN

### STAFF USE ONLY

Date completed: 1-13-00  
Searcher: HDR 308-7795  
Terminal time: 160  
Elapsed time: 30  
CPU time: \_\_\_\_\_  
Total time: 190  
Number of Searches: 1  
Number of Databases: 54

Search Site  
☒ STIC/EIC  
☐ CM-1  
☐ Pre-S  
Type of Search  
☐ N.A. Sequence  
☐ A.A. Sequence  
☐ Structure  
☐ Bibliographic

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☐ Other

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File 34:SciSearch(R) Cited Ref Sci 1990-2000/Jan W2  
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File 99:Wilson Appl. Sci & Tech Abs 1983-1999/Nov  
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Set	Items	Description
S1	7234	IC()CARD? ? OR INTEGRATED()CIRCUIT()CARD? ? OR CHIP()CARD? ? OR SMART()CARD? ? OR INTELLIGENT()CARD? ?
S2	326408	(MULTIPLE OR MULTI? ? OR MORE()THAN()ONE OR SEVERAL OR PLU- RALITY OR TWO OR DIFFERENT OR VARIOUS)(3W)(PROTOCOL? ? OR MOD- E? ?)
S3	1347763	READER? ? OR INTERFACE? ? OR CONTACTS OR SWIPER
S4	0	S1(15N)S2(15N)S3
S5	2	S1 AND S2 AND S3
S6	2	RD (unique items)
S7	30	S1 AND S2
S8	26	RD (unique items)
S9	26	S6 OR S8

?t9/7/all

9/7/1 (Item 1 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)  
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04578491 E.I. No: EIP96123470937

**Title: Design of an adaptive controller for A DC motor within an existing PLC framework**

Author: Ghandakly, Adel A.; Shields, Mark E.; Brihoum, Mohamed E.  
Corporate Source: Univ of Toledo, Toledo, OH, USA  
Conference Title: Conference Record of the 1996 IEEE Industry Applications 31th IAS Annual Meeting. Part 3 (of 4)  
Conference Location: San Diego, CA, USA Conference Date: 19961006-19961010  
Sponsor: IEEE  
E.I. Conference No.: 45752  
Source: Conference Record - IAS Annual Meeting (IEEE Industry Applications Society) v 3 1996. IEEE, Piscataway, NJ, USA, 96CH35977. p 1567-1574

Publication Year: 1996  
CODEN: CIASDZ ISSN: 0197-2618  
Language: English  
Document Type: CA; (Conference Article) Treatment: A; (Applications); T  
; (Theoretical)  
Journal Announcement: 9702W1

Abstract: This paper aims at the development of a technique for incorporating an Intelligent Adaptive Controller based on the Self Tuning Regulator (STR) technology into an existing industrial Programmable Logic Controllers (PLC) for a dc motor. The adaptive controller is based on an intelligent parallel regulator and a Parameter Optimization (PO) technique. The PLC used is an Allen Bradley (AB) PLC5 system. The adaptive controller will be incorporated in the overall PLC program. The effectiveness of proposed controller is shown by two simulation studies on the motor under **different** operating **modes**. (Author abstract) 10 Refs.

9/7/2 (Item 2 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)  
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04552478 E.I. No: EIP96110403169

**Title: 'System for monitoring the electric power supply system and for voltage and current transients recording'**

Author: Aramendi, E.; Ruiz, J.; Leturiondo, L.; Lazcano, A.  
Corporate Source: Universidad del Pais Vasco, Bilbao, Spain  
Conference Title: Proceedings of the 1996 8th Mediterranean Electrotechnical Conference, MELECON'96. Part 2 (of 3)  
Conference Location: Bari, Italy Conference Date: 19960513-19960516  
Sponsor: IEEE  
E.I. Conference No.: 45520  
Source: Industrial Applications in Power Systems, Computer Science and Telecommunications Proceedings of the Mediterranean Electrotechnical Conference - MELECON v 2 1996. IEEE, Piscataway, NJ, USA, 96CH35884. p 908-912

Publication Year: 1996  
CODEN: PMECFA  
Language: English  
Document Type: CA; (Conference Article) Treatment: A; (Applications); X  
; (Experimental)  
Journal Announcement: 9701W1

Abstract: This paper presents a new system for two important applications of digital recording in real time characterisation of electric signals. The first one consists of a digital recorder of the time evolution of voltage signals working in **two** **modes**: recording directly the evolution of the

signal waveform or the evolution of the main electric parameters: rms., power components and frequency values. The second application consist of a digital recorder of voltage dips. Both applications are based on the same hardware that includes a PC with two DSP cards, which make possible a real time working multichannel system. Examples of field measurements performed using a prototype as the one described in the paper are included. (Author abstract) 14 Refs.

9/7/3 (Item 3 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)  
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04254303 E.I. No: EIP95092862733

**Title: Advanced synchronous communication PC-add on card**

Author: Arivazhagan, S.

Corporate Source: Mepco Schlenk Engineering Coll, Amathur, India

Source: IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India) v 12 n 2 Mar-Apr 1995. p 137-141

Publication Year: 1995

CODEN: ITREEI ISSN: 0256-4602

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 9511W3

Abstract: Serial Communication is one area where a lot of improvement is desirable. This paper describes an advanced synchronous communication PC/PC-XT/PC-AT add on card, designed and developed for SHAR computer facilities, SHAR centre, ISRO, Sriharikota, to provide synchronous communication in personal computers. This card is developed as an intelligent one, having on-board CPU, memory and interrupt controller to save the PC CPU time. Two independent full duplex channels, fully compatible with RS232-C standards with maximum speed of 19200 bps are provided by this card. The on-board firmware allows to program the channels in asynchronous/synchronous mode of communication, at different baud rates (50 bps to 19200 bps) and in **different operation modes**. A driver software is provided to run in PC to transmit/receive the data to/from the external world. Also a diagnostic software is provided to run in PC as well as in the card to test all the major components or hardware subsystems in the card individually. (Author abstract) 10 Refs.

9/7/4 (Item 4 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)  
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04238333 E.I. No: EIP95082834379

**Title: Interworking between GSM and PDC through IC cards**

Author: Nodera, Y.; Ohashi, M.; Sakai, S.; Suzuki, T.; Yamaguchi, A.; Mizuno, T.

Corporate Source: KDD R&D Lab

Conference Title: Proceedings of the 1995 IEEE International Conference on Communications. Part 2 (of 3)

Conference Location: Seattle, WA, USA Conference Date: 19950618-19950622

Sponsor: IEEE

E.I. Conference No.: 43480

Source: IEEE International Conference on Communications v 2 1995. IEEE, Piscataway, NJ, USA, 95CH35749. p 761-765

Publication Year: 1995

CODEN: 002115

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications); T

; (Theoretical)

Journal Announcement: 9510W4

Abstract: This paper addresses roaming technique between GSM and PDC cellular systems using the **different** signaling **protocols**. Many countries have already employed GSM specifications in their PLMNs and these systems inter-work effectively. In PDC, a similar concept of card services will be introduced in the future. The protocols for this services use a PDC card and guarantee PM (Personal Mobility) and TM (Terminal Mobility). In this paper, we introduce the service images come up with through roaming between different cellular standards and propose a system configuration that realizes relevant services. We propose a detailed system configuration which supports roaming feature provision. Then we propose an authentication technique to realize these services. (Author abstract)

9/7/5 (Item 5 from file: 8)

DIALOG(R) File 8:EI Compendex(R)

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04238262 E.I. No: EIP95082834308

**Title: Higher performance and implementation independence: downloading a protocol onto a communication card**

Author: Budhia, R.K.; Melliar-Smith, P.M.; Moser, L.E.; Miller, Robert

Corporate Source: Univ of California, Santa Barbara, CA, USA

Conference Title: Proceedings of the 1995 IEEE International Conference on Communications. Part 1 (of 3)

Conference Location: Seattle, WA, USA Conference Date: 19950618-19950622

Sponsor: IEEE

E.I. Conference No.: 43480

Source: IEEE International Conference on Communications v 1 1995. IEEE, Piscataway, NJ, USA, 95CH35749. p 385-389

Publication Year: 1995

CODEN: 002115

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications)

Journal Announcement: 9510W4

Abstract: Operating system costs form a substantial part of the overhead faced by communication protocols. Running the protocol on board a communication **interface** card, which has a simple, real-time operating system eliminates many of these overheads and gives rise to higher throughput. We describe a novel approach of downloading a protocol, in the form of STREAMS modules, onto such cards. This implementation is also independent of the communication **interface** card and allows the use of parts of the same or **different protocols** to meet varying service requirements. (Author abstract) 12 Refs.

9/7/6 (Item 6 from file: 8)

DIALOG(R) File 8:EI Compendex(R)

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04176397 E.I. No: EIP94122458928

**Title: Combo cards pose a wide array of choices for both product managers and design engineers**

Author: Parrish, Tom

Source: IC Card Systems & Design v 4 n 5 July-Aug 1994. p 28-29

Publication Year: 1994

CODEN: ICSDE3 ISSN: 1055-5188

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 9508W1

Abstract: The combo cards have created quite a stir in the PC Card market. In a communications-centric world, the combo card offers the mobile user the convenience of rarely having to swap cards at all. However, the multi-function cards are not confined to area of communications since there are practically unlimited range of functions that may be combined. Combo cards provide a clear path to product differentiation, however the range of implementation choices present a possible threat to the compatibility stability that PCMCIA evidently requires.

9/7/7 (Item 7 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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03492325 E.I. Monthly No: EI9210124395

**Title: Passports and visas versus IDs.**

Author: Davida, George I.; Desmedt, Yvo G.

Corporate Source: Univ of Wisconsin-Milwaukee, Milwaukee, WI, USA

Source: Computers & Security v 11 n 3 May 1992 p 253-258

Publication Year: 1992

CODEN: CPSEDU ISSN: 0167-4048

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review); A; (Applications); T; (Theoretical)

Journal Announcement: 9210

Abstract: Identification is an extremely important issue in today's world. The need to identify humans, machines, messages and even animals has created demands for a variety of requirements that identification systems must adhere to. While institutions and governments have a large stake in identification schemes and their efficacy, individuals also have a need to identify institutions, machines or other individuals. One of the most widely used schemes for identification is the passport. Passports are used by the world community to enforce various protocols for travel, work and other cooperative ventures. Passports are used too by governments to prevent undesirable individuals from entering their respective countries. The passport is the prime culprit in the inability of countries to control terrorism. To improve the security of identification, it has been proposed that cryptography be used to enhance the security of verifying the identity of an individual. However, most of the proposed cryptography-based electronic IDs are not adequate when used in international identification protocols. In this paper we extend the concept of a cryptographic electronic ID to a system of electronic passports and visas that are superior to existing paper versions with respect to security. (Author abstract) 8 Refs.

9/7/8 (Item 8 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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03485032 E.I. Monthly No: EIM9209-044435

**Title: Card sending linear ultrasonic motor using multi-beam piezoelectric vibrators.**

Author: Kosawada, T.; Suzuki, K.; Tomikawa, T.

Corporate Source: Yamagata Univ, Yonezawa, Jpn

Conference Title: 3rd International Symposium on the Application of Electromagnetic Forces

Conference Location: Sendai, Jpn Conference Date: 19910128

E.I. Conference No.: 16543

Source: International Journal of Applied Electromagnetics in Materials v 2 n 4 Apr 1992. p 285-290

Publication Year: 1992

CODEN: 222208 ISSN: 0925-2096  
Language: English  
Document Type: JA; (Journal Article) Treatment: T; (Theoretical); X;  
(Experimental)

Journal Announcement: 9209

Abstract: A new linear type ultrasonic motor using multibeam piezoelectric vibrators is presented. The standing waves of the eighth bending mode of vibration and the first longitudinal mode of vibration are utilized to construct a card-sending mechanism. All the **multiple different form modes** are excited in different vibrators, and then these vibrators are combined as one ultrasonic motor. These vibrators are excited electrically by using piezoelectric ceramics. Some characteristics of this ultrasonic motor, as a card-sending device, are investigated. It is observed that the motor has enough power and speed as a card-sending device as well as a self-moving motor. (Edited author abstract) 7 Refs.

9/7/9 (Item 1 from file: 35)

DIALOG(R)File 35:Dissertation Abstracts Online  
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01679284 ORDER NO: AAD99-13083

**COMPUTATIONAL MODELING OF THERMAL MANAGEMENT IN ELECTRONIC PACKAGING DESIGN AND OPERATIONS (COMPUTATIONAL FLUID DYNAMICS)**

Author: WEI, WEN

Degree: PH.D.

Year: 1998

Corporate Source/Institution: OREGON GRADUATE INSTITUTE OF SCIENCE & TECHNOLOGY (0284)

Supervisor: LEMMY L. MEEKISHO

Source: VOLUME 59/11-B OF DISSERTATION ABSTRACTS INTERNATIONAL.  
PAGE 6043. 208 PAGES

Thermal phenomena associated with electronic packaging were introduced and explored in detail. Packaging refers to the silicon integrated circuits (IC), **cards** and boards. The mechanisms of **multi-mode** heat transfer in electronic packaging are summarized and approaches to the design and operation by means of conjugate thermal-flow simulations and testing are explored. Simulations were accomplished by numerical modeling with the aid of finite element method (FEM) and finite volume method (FVM) techniques.

The component level designs included the structural design and material management. Structural designs focused on the substrate, die, lid, board, thermal vias, heat sink, adhesive layer design. These configurations are associated with thermal conductivity, convective heat transfer coefficient, emissivity, view factor and other parameters which involve conduction, convection and radiation heat transfer over the IC components, ASICs and other packaging components. Material selections were based on the thermal conductivity performance for IC substrate, die, thermal vias, printed circuit board (PCB), and interfacial materials between heat sinks and lid, die and substrate. A three dimensional (3D) simulation case study of a multi-chip module (MCM) in surface mounted technology (SMT) ball-grid-array (BGA) hybrid packaging on multi-layer printed circuit boards illustrated the component level design.

Device system level designs are mainly associated with forced convection over all components and board level systems. 3D computational fluid dynamics (CFD) FVM models were used to compute system level solutions numerically. These numerical solutions were compared with experimental results. The models involved arrays of electronics modules in a channel.

The simulations involved conduction heat transfer, conjugate conduction/flow, convection and radiation heat transfer. The flows were assumed to be viscous and incompressible laminar or turbulent fluid flow conjugated with heat conduction or radiation. In computational fluid

dynamics of system models for turbulence flow, the k- $\epsilon$ ; and LVEL algebraic turbulence models were used. Application of different thermal and flow boundaries including interfacial thermal resistance were explored and a discussion is initiated. The relative error of simulation results were found to lie in the range 0.64%–7.67% in comparison with standard benchmark tests. Future trends of thermal management issues as they apply to electronic packaging are discussed.

9/7/10 (Item 1 from file: 111)

DIALOG(R) File 111:TGG Natl.Newspaper Index(SM)  
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05571980 Supplier Number: 53078569

**Microcell Expands Relationship With Schlumberger; Purchases Several Hundred Thousand Dual Mode SIM Cards.**

Business Wire, 0129  
Oct 13, 1998

9/7/11 (Item 1 from file: 2)

DIALOG(R) File 2:INSPEC  
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6352133 INSPEC Abstract Number: C1999-10-7120-075

**Title: A framework for smart card payment on Internet**

Author(s): Hung, P.; Ieong, W.S.C.

Author Affiliation: Hong Kong Univ. of Sci. & Technol., Hong Kong

Conference Title: Hong Kong International Computer Conference. Ready for the Electronic Society Part vol.3 p.75-81 vol.3

Publisher: Hong Kong Comput. Soc, Hong Kong

Publication Date: 1998 Country of Publication: Hong Kong 3 vol. (63+80+84) pp.

Material Identity Number: XX-1999-01498

Conference Title: Proceedings of 21st Hong Kong International Computer Conference

Conference Date: 16-17 Sept. 1998 Conference Location: Hong Kong

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P)

Abstract: The Internet is a new media for business activity or electronic commerce. When we are talking about business activity on the Web, the focus is on the fact that the traditional tools which are being used for business activity in our society every day are no longer suitable for the Internet environment. We need to design and develop another set of cyber tools such as protocols and systems to conduct business activity on the Web. With the popularity of Web technology, there is a trend that the **smart card** is going to be used as an electronic wallet to hold money for business activity on the Web. The Internet is largely insecure because of its open access for the public, although **several** secure **protocols** are in use. These days, the **smart card** is becoming popular in many different applications, especially for payment transactions like Mondex, DigiCash or CyberCash. But none of these frameworks support untraceable secure digital money. Moreover, most of the related works of **smart card** payment on the Web only concentrates on the security aspects of hardware/firmware, encryption method and key management, or they only propose the online shopping protocol for secure exact payment. Furthermore, the shopping protocols proposed so far do not support negotiation, bargaining or privacy among parties. The main focus of the paper is to present a framework of SmartFlow by using a **smart card** to process payment transactions on the Internet. We also discuss the security issues related to this work. We have implemented the prototype system, SmartFlow, to demonstrate the concepts developed in the paper. (23 Refs)



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9/7/12 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

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6314249 INSPEC Abstract Number: B1999-09-6120D-017, C1999-09-1260C-015

**Title: Secure password-based protocol for downloading a private key**

Author(s): Perlman, R.; Kaufman, C.

Author Affiliation: Sun Microsyst. Labs., Chelmsford, MA, USA

Conference Title: Proceedings 1999 Network and Distributed System Security Symposium p.3-11

Publisher: Internet Soc, Reston, VA, USA

Publication Date: 1999 Country of Publication: USA x+171 pp.

ISBN: 1 891562 04 5 Material Identity Number: XX-1999-00579

Conference Title: Proceedings of The Internet Society 1999 Network and Distributed System Security Symposium

Conference Sponsor: Internet Soc

Conference Date: 3-5 Feb. 1999 Conference Location: San Diego, CA, USA

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: We present protocols that allow a user Alice, knowing only her name and password, and not carrying a **smart card**, to "log in to the network" from a "generic" workstation, i.e., one that has all the necessary software installed, but none of the configuration information usually assumed to be known a priori in a security scheme, such as Alice's public and private keys, her certificate, and the public keys of one or more CAs. By "logging in", we mean the workstation retrieves this information on behalf of the user. This would be straightforward if Alice had a cryptographically strong password. We propose protocols that are secure even if Alice's password is guessable. We concentrate on the initial retrieval of Alice's private key from some server Bob on the network. We discuss **various protocols** for doing this that avoid off-line password guessing attacks by someone eavesdropping or impersonating Alice or Bob. We discuss auditable vs. unauditable on-line attacks, and present protocols that allow Bob to be stateless, avoid denial-of-service attacks, allow for salt, and are minimal in computation and number of messages. (11 Refs)

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9/7/13 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

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6112312 INSPEC Abstract Number: B9901-6120D-067, C9901-1260C-062

**Title: The Beguin-Quisquater server-aided RSA protocol from Crypto '95 is not secure**

Author(s): Nguyen, P.; Stern, J.

Author Affiliation: Lab. d'Inf., Ecole Normale Superieure, Paris, France

Conference Title: Advances in Cryptology - ASIACRYPT '98. International Conference on the Theory and Applications of Cryptology and Information Security. Proceedings p.372-9

Editor(s): Ohta, K.; Pei, D.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1998 Country of Publication: Germany xii+436 pp.

ISBN: 3 540 65109 8 Material Identity Number: XX98-02821

Conference Title: Advances in Cryptology - ASIACRYPT '98. International Conference on the Theory and Application of Cryptology and Information Security. Proceedings

Conference Sponsor: State Key Lab. Inf. Security; Univ. Sci. & Technol. China; Asiacrypt Steering Committee

Conference Date: 18-22 Oct. 1998      Conference Location: Beijing, China  
Language: English      Document Type: Conference Paper (PA)  
Treatment: Theoretical (T); Experimental (X)

Abstract: A well-known cryptographic scenario is the following: a **smart card** wishes to compute an RSA signature with the help of an untrusted powerful server. **Several protocols** have been proposed to solve this problem, and many have been broken. There exist two kinds of attacks against such protocols: passive attacks (where the server follows the instructions) and active attacks (where the server may return false values). An open question in this field is the existence of efficient protocols (without expensive precomputations) provably secure against both passive and active attacks. At Crypto 95, Beguin and Quisquater tried to answer this question by proposing an efficient protocol which was resistant against all known passive and active attacks. We present a very effective lattice-based passive attack against this protocol. An implementation is able to recover the secret factorization of an RSA-512 or RSA-768 key in less than 5 minutes once the card has produced about 50 signatures. The core of our attack is the basic notion of an orthogonal lattice which we introduced at Crypto 97 as a cryptographic tool. (15 Refs)

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9/7/14      (Item 4 from file: 2)

DIALOG(R)File    2:INSPEC

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6033916      INSPEC Abstract Number: B9811-6120B-029, C9811-6130S-024

**Title: On the security of server-aided RSA protocols**

Author(s): Merkle, J.; Werchner, R.

Author Affiliation: Frankfurt Univ., Germany

Conference Title: Public Key Cryptography. First International Workshop on Practice and Theory in Public Key Cryptography, PKC'98. Proceedings p.99-116

Editor(s): Imai, H.; Zheng, Y.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1998      Country of Publication: Germany      xi+262 pp.

ISBN: 3 540 64693 0      Material Identity Number: XX98-01841

Conference Title: Public Key Cryptography First International Workshop on Practice and Theory in Public Key Cryptography, PKC'98 Proceedings

Conference Sponsor: Inf.-Technol. Promotion Agency (IPA); Mitsubishi Electr.; Univ. Tokyo; et al

Conference Date: 5-6 Feb. 1998      Conference Location: Pacifico Yokohama, Japan

Language: English      Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: We investigate the security of the server-aided RSA protocol RSA-S1 and RSA-S1M proposed by Matsumoto et al (1989) and Matsumoto et al. (1993) respectively. In these protocols a **smart card** calculates an RSA signature with the aid of an untrusted powerful server. We focus on generic attacks, that is, passive attacks that do not exploit any special properties of the encoding of the group elements. Generic algorithms have been introduced by Nechaev (1994) and Shoup (1997). We prove lower bounds for the complexity of generic attacks on these **two protocols** and show that the bounds are sharp by describing attacks that almost match our lower bounds. To the best of our knowledge these are the first security proofs for efficient server-aided RSA protocols. (16 Refs)

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9/7/15      (Item 5 from file: 2)

DIALOG(R)File    2:INSPEC

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5125226 INSPEC Abstract Number: B9601-6120B-069, C9601-6130S-065

**Title: Security and performance of server-aided RSA computation protocols**

Author(s): Lim, C.H.; Lee, P.J.

Author Affiliation: Dept. of Electr. Eng., Pohang Univ. of Sci. & Technol., South Korea

Conference Title: Advances in Cryptology - CRYPTO '95. 15th Annual International Cryptology Conference. Proceedings p.70-83

Editor(s): Coppersmith, D.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1995 Country of Publication: West Germany xii+465 pp.

ISBN: 3 540 60221 6

Conference Title: Proceedings of CRYPTO '95: 15th Annual Crypto Conference

Conference Sponsor: Int. Assoc. Cryptologic Res

Conference Date: 27-31 Aug. 1995 Conference Location: Santa Barbara, CA, USA

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: This paper investigates various security issues and provides possible improvements on server-aided RSA computation schemes, mainly focused on the **two**-phase **protocols**, RSA-S1M and RSA-S2M, proposed by Matsumoto et al (1993). We first present new active attacks on these protocols when the final result is not checked. A server-aided protocol is then proposed in which the client can check the computed signature in at most six multiplications irrespective of the size of the public exponent. Next we consider multi-round active attacks on the protocol with correctness check and show that parameter restrictions cannot defeat such attacks. We thus assume that the secret exponent is newly decomposed in each run of the protocol and discuss some means of speeding up this preprocessing step. Finally, considering the implementation-dependent attack, we propose a new method for decomposing the secret and performing the required computation efficiently. (23 Refs)

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9/7/16 (Item 6 from file: 2)

DIALOG(R)File 2:INSPEC

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4566498 INSPEC Abstract Number: B9402-6120B-063, C9402-6130S-033

**Title: Two efficient server-aided secret computation protocols based on the addition sequence**

Author(s): Chi-Sung Lai; Sung-Ming Yen; Lein Harn

Author Affiliation: Dept. of Electr. Eng., Nat. Cheng Kung Univ., Tainan, Taiwan

Conference Title: Advances in Cryptology - ASIACRYPT '91. International Conference on the Theory and Application of Cryptology Proceedings p. 450-9

Editor(s): Imai, H.; Rivest, R.L.; Matsumoto, T.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1993 Country of Publication: West Germany x+498 pp.

ISBN: 3 540 57332 1

Conference Sponsor: Int. Assoc. Cryptologic Rec.; Inst. Electron. Inf. & Common. Engineers

Conference Date: 11-14 Nov. 1991 Conference Location: Fujiyoshida, Japan

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P)

Abstract: A server-aided secret computation protocol (SASC) is a method that allows a client (e.g. **smart card**) to compute a function

efficiently with the aid of a powerful server (e.g. compute) without revealing the client's secrets to the server. T. Matsumoto et al. (1988) proposed a solution to the problem which is suitable for the RSA cryptosystem. S. Kawamura et al. (1989) have shown that a client with a  $10^5$  times more powerful server's aid, can compute an RSA signature 50 times faster than the case without a server if the communication cost can be ignored. The authors propose **two SASC protocols** based on the addition sequence to improve the efficiency. In the first protocol, since the addition sequence is determined by the server, it can improve the computational efficiency of the server only and it is suitable for the low speed communication link (e.g. 9.6 Kbps). It is expected that a client, with a 8982 times more powerful server's aid, can compute an RSA signature 50 times faster than the case without a server. In the second protocol, since the addition sequence is determined by the client, it can improve the computational efficiency of the client and sever simultaneously but takes more communication time and it is suitable for the high speed communication link (e.g. above 10 Mbps). It is expected that a client, with a 3760 times more powerful server's aid, can compute an RSA signature 200 times faster than the case without a server. (11 Refs)

9/7/17 (Item 7 from file: 2)

DIALOG(R)File 2:INSPEC

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4422829 INSPEC Abstract Number: B9307-6120B-055, C9307-6130S-066

**Title: Attacks on protocols for server-aided RSA computation**

Author(s): Pfitzmann, B.; Waidner, M.

Author Affiliation: Inst. fur Inf. Hildersheim Univ., Germany

Conference Title: Advances in Cryptology - EUROCRYPT '92. Workshop on the Theory and Applications of Cryptographic Techniques. Proceedings p. 153-62

Editor(s): Rueppel, R.A.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 1993 Country of Publication: West Germany x+491 pp.

ISBN: 3 540 56413 6

Conference Date: 24-28 May 1992 Conference Location: Balatonfured, Hungary

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: Matsumoto, Kato, and Imai (1988) presented protocols to speed up secret computations with insecure auxiliary devices. The **two** most important **protocols** enable a **smart card** to compute the secret RSA operation faster with the help of a server that is not necessarily trusted by the card holder. It was stated that if RSA is secure, the protocols could only be broken by exhaustive search in certain spaces. The main attacks show that much smaller search spaces suffice. These attacks are passive and therefore undetectable. It was already known that one of the protocols is vulnerable to active attacks. The authors show that this holds for the other protocol, too. More importantly, they show that attack may still work if the **smart card** checks the correctness of the result; this was previously believed to be an easy measure excluding all active attacks. Finally, they discuss attacks on related protocols. (16 Refs)

9/7/18 (Item 8 from file: 2)

DIALOG(R)File 2:INSPEC

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03876507 INSPEC Abstract Number: B91035958

**Title: Smart card metering**

Author(s): Faulkner, W.E.

Author Affiliation: Midlands Electr. Plc., Halesowen, UK  
Conference Title: Smart Card '91 International Exhibition p.8 pp.  
vol.1

Publisher: Agestream Ltd, Peterborough, UK  
Publication Date: 1991 Country of Publication: UK 3 vol. 580 pp.  
Conference Date: 12-14 Feb. 1991 Conference Location: London, UK  
Language: English Document Type: Conference Paper (PA)  
Treatment: Practical (P)

Abstract: The card comprises a credit card sized plastic card containing an 8 bit micro controller, an operating system occupying 3 kbytes of ROM and 2 kbytes E/sup 2/ Prom for use by the electricity utility and others. Communication and power are provided by an induction loop in the card being placed in an RF, field generated by the meter or the credit dispenser. The data transmission to and from the card is achieved by modulation of the field in **two modes**, amplitude modulation for transmission from the card to the meter and dispenser and frequency shift keying for transmission from the meter and dispenser to the card. The E/sup 2/ Prom is divided into blocks of 28 bytes per block and each block can be protected from unauthorised access to information contained within the block. (0 Refs)

9/7/19 (Item 9 from file: 2)

DIALOG(R)File 2:INSPEC

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03777473 INSPEC Abstract Number: B90077738, C91004276

**Title: Secure user access control for public networks**

Author(s): Pil Joong Lee

Author Affiliation: Bell Commun. Res., Morristown, NJ, USA

Conference Title: Advances in Cryptology-AUSCRYPT '90 International  
Conference on Cryptology. Proceedings p.46-57

Editor(s): Seberry, J.; Pieprzyk, J.

Publisher: Springer-Verlag, Berlin, West Germany

Publication Date: 1990 Country of Publication: West Germany ix+462  
pp.

ISBN: 3 540 53000 2

Conference Date: 8-11 Jan. 1990 Conference Location: Sydney, NSW,  
Australia

Language: English Document Type: Conference Paper (PA)

Treatment: Theoretical (T)

Abstract: A secure and convenient user identity authentication method is presented that provides the users with evidence of mutual authentication, based on public-key cryptographic techniques. This **two-round protocol** assumes that each user has a personalized card issued by a trusted certification center and uses it at a user access terminal. This protocol provides a signed session key for users who want protection for their communications with a conventional one-key cryptosystem at no extra cost. By the use of an identity certificate, the need for a trusted public-key directory is eliminated. For the card issuing process, **two initialization protocols** were considered with dumb cards and with **smart cards**. For the **smart card** case, it is shown that users' secrets need not be exposed even to the trusted certification center. (10 Refs)

9/7/20 (Item 1 from file: 94)

DIALOG(R)File 94:JICST-EPLUS

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03827601 JICST ACCESSION NUMBER: 98A0918884 FILE SEGMENT: JICST-E  
**Development of Multi tag Access Protocol. Reduced Communications time  
and Downsizing of ASIC.**

OKUNO YOSHITO (1)

(1) Omron Corp.

Omron Tech, 1998, VOL.38,NO.3, PAGE.275-277, FIG.4, TBL.1

JOURNAL NUMBER: S0266AAU ISSN NO: 0474-1315 CODEN: OMTKA

UNIVERSAL DECIMAL CLASSIFICATION: 621.382.2/.3.049.77 621.37+

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Journal

ARTICLE TYPE: Original paper

MEDIA TYPE: Printed Publication

ABSTRACT: OMRON has developed a **multi** -tag access **protocol** for the purpose of reducing the communications time of the RFID System operating at a carrier frequency of 125kHz. (1) Encoded I/D Number The I/D number response from each tag to the R/W antenna is partly encoded. This makes it possible to identify each block of response data if more than one block of response data are received by the R/W antenna. (2) Designation of Part of I/D Number A part of an I/D number is designated by the R/W antenna. The tag sends the whole I/D number, provided that the part of the I/D number coincides with the one designated by the R/W antenna. (3) Unique I/D Number Each tag has its own unique I/D number allocated at random. This protocol makes it possible to downsize the ASIC of each tag and reduce tag communications time. (author abst.)

9/7/21 (Item 2 from file: 94)

DIALOG(R)File 94:JICST-EPLUS

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02557850 JICST ACCESSION NUMBER: 95A0779878 FILE SEGMENT: JICST-E

**PHS Business Strategies for a Multimedia Society.**

IWAMOTO YOSHINAO (1)

(1) Asuterutokyo

Tsushin Kogyo(CIAJ Journal (Communications Industry Association of Japan), 1995, VOL.35,NO.8, PAGE.12-14, FIG.1

JOURNAL NUMBER: G0903AAA ISSN NO: 0041-381X

UNIVERSAL DECIMAL CLASSIFICATION: 621.396.73 621.394/.395

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Journal

ARTICLE TYPE: Commentary

MEDIA TYPE: Printed Publication

ABSTRACT: ASTEL Tokyo Corporation will provide the PHS service in October, 1995, which is a new mobile communication system. We introduce our technical development issues for the coming multi-media age, which include PHS network, **multi** -media terminal, dual **mode** terminal, **IC card** , information provision service, and PBX with cordless telephone. (author abst.)

9/7/22 (Item 3 from file: 94)

DIALOG(R)File 94:JICST-EPLUS

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02048849 JICST ACCESSION NUMBER: 94A0403675 FILE SEGMENT: JICST-E

**Research of a practical system of calculation on request in which the computer makes a calculation driven from the main problems. Fiscal 1993.( Sponsor : Ministry of Education ).**

IMAI HIDEKI (1)

(1) Inst. of Ind. Sci., Univ. of Tokyo

Shin ni Tokitai Mondai o Kakushitsutsu Keisanki no Chikara o Riyo suru Jitsuyoteki na Irai Keisan Hoshiki no Kenkyu. Heisei 5 Nendo.

No.04402033, 1994, PAGE.64P

JOURNAL NUMBER: N19941071P

UNIVERSAL DECIMAL CLASSIFICATION: 681.3:519.6

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Journal

ARTICLE TYPE: Original paper

MEDIA TYPE: Printed Publication

ABSTRACT: A general-purpose protocol of calculation on request which can deal with a large group of individual problems was made to solve the problems by simply applying it to individual applications. In **various** fields, the **protocol** may improve the abilities of the instruments such as **IC cards** and personal computers are relatively low in calculation ability. In this report, development and mounting experiment of a protocol of calculation on request for convex planning problem are reported. Publication items are as follows.1) Approach to the general-purpose request calculation.2) Theoretical preparation for checks.3) Deformation method of the individual problem for keeping secret.4) Request calculation protocol for a convex planning problem.5) Effectiveness of a request calculation protocol of convex planning problem, etc.

9/7/23 (Item 4 from file: 94)

DIALOG(R) File 94:JICST-EPLUS

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01783136 JICST ACCESSION NUMBER: 93A0453333 FILE SEGMENT: JICST-E  
**Instrumentation of a Novel Simultaneous Multiple Peptide Synthesizer,**  
Model **PSSM-8**.

NOKIHARA KISHIYO (1); YAMAMOTO RINTARO (1); HAZAMA MAKOTO (1); NAKAMURA SHIN (1); YAMAGUCHI MINORU (1)

(1) Shimadzu Corp.

Shimazu Hyoron(Shimadzu Review), 1993, VOL.50,NO.1, PAGE.33-43, FIG.10, TBL.3, REF.12

JOURNAL NUMBER: F0302AAM ISSN NO: 0371-005X CODEN: SHHYA

UNIVERSAL DECIMAL CLASSIFICATION: 577.112:54-114

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Journal

ARTICLE TYPE: Review article

MEDIA TYPE: Printed Publication

ABSTRACT: The importance of simultaneous multiple peptide synthesis is now significantly increasing for the rapid screening or evaluation of peptides such as epitopes, agonists, antagonists or more potent structures. An eight channel automated simultaneous solid phase peptide synthesizer, Model PSSM-8, has been constructed, which has wide flexibility for syntheses. The flexibility of this synthesizer allows different length and/or **different** chemical **protocols** in addition to the synthesis of different peptides. Therefore this instrument can be used for the rapid evaluation of the synthetic chemistry or reaction conditions as well as studies of epitopes or structure activity relationships. Eight independent channel allows cross-contamination-free syntheses and the generation of variable amounts (0.005-0.5mmol/run) of high quality peptides. Two software packages were developed (a calculation program for syntheses and an operation program which controls the functions of the valves, times, and injection volume). Using a conventional personal computer, created parameter sets can be stored on the IC-memory card via the Model BMD-1. The PSSM-8 can be automatically operated by this card; although the PSSM-8 has its own micro-processor, and can be operated manually without the **IC -card** . For simultaneous cleavage, PSSM-C8 was also constructed. As cleavage can be easily performed using the same reaction vessels, the mechanical loss at the cleavage stage can be minimized in small scale syntheses. The present synthesizer is suitable for the rapid production of various high quality peptides such as neuropeptides, hormones as well as antigens. (author abst.)

9/7/24 (Item 1 from file: 233)

DIALOG(R)File 233:MICROCOMPUTER ABSTRACTS

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00546942 99PK09-102

**Network agendas: availability, security; VPN vendors to show Win 2000 interoperability**

Kerstetter, Jim

PC Week , September 13, 1999 , v16 n37 p1, 18, 2 Page(s)

ISSN: 0740-1604

Discusses interoperability between Microsoft Windows 2000 and virtual private networks (VPNs). Explains that all implementations of Windows 2000 on VPN products from Cisco Systems Inc., Altiga Networks Inc., 3Com Corp., RouterWare Inc., and Lucent Technologies Inc., are based on a combination of IPSecurity (IPSec) and Layer 2 Tunneling Protocol (L2TP) specifications. Cautions that Windows 2000 VPN tunnels will not interoperate with all existing IPSec hardware and software and will not support multicasting or remote access on IPSec. Concludes that the combination of IPSec and L2TP is superior and supports authentication of **smart cards**, passwords, or other user authorization. Adds that it also simplifies assigning IP addresses and supports **multiple transmission protocols**. Includes one table. (amg)

9/7/25 (Item 2 from file: 233)

DIALOG(R)File 233:MICROCOMPUTER ABSTRACTS

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00297942 92MF12-009

**Casio Executive B.O.S.S. SF-R20**

Meresman, Michael

Mobile Office , December 1, 1992 , v3 n12 p48-52, 3 Page(s)

ISSN: 1047-1952

Company Name: Casio

Product Name: Casio Executive B.O.S.S. SF-R20

Presents a favorable review of the Casio Executive B.O.S.S. SF-R20 (\$449), a handheld personal information organizer from Casio, Inc. of Dover, NJ (800, 201). Says the B.O.S.S. has a large, 10 x 40-character screen, and 256K memory. **IC cards** are available to expand its range of applications. Describes B.O.S.S.'s annual, monthly, or daily event memory; its capability for entry of multiple-date entries; and the telephone and business card directories. Calls the spreadsheet mode impressive, and says that B.O.S.S. has **two display modes** for viewing records. Also praises its display, and notes that two B.O.S.S.es can be linked with a s cable. Two AAA batteries should last over 100 hours. Howev memo mode allows only 384 characters per entry, and B.O.S.S. is n DOS-compatible. Includes one photo and one illustration. (jo)

9/7/26 (Item 1 from file: 6)

DIALOG(R)File 6:NTIS

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1885011 NTIS Accession Number: PB95-221222

**Smart Cards for Transit: Multi-Use Remotely Interrogated Stored-Data Cards for Fare and Toll Payment**

(Final rept. Jan 93-Mar 94)

Bushnell, W. R.

John A. Volpe National Transportation Systems Center, Cambridge, MA.  
Research and Special Programs Administration.

Corp. Source Codes: 098811002;



Ginger Roberts - Search Report

Sponsor: Coopers and Lybrand, Cambridge, MA.; Federal Transit Administration, Washington, DC. Rural Transit Assistance Program.

Report No.: DOT-VNTSC-FTA-95-2; FTA-MA-26-0020-95-1

Apr 95 139p

Languages: English

Journal Announcement: GRAI9517

Prepared in cooperation with Coopers and Lybrand, Cambridge, MA. Sponsored by Federal Transit Administration, Washington, DC. Rural Transit Assistance Program.

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NTIS Prices: PC A07/MF A02

Country of Publication: United States

Contract No.: DTRS-57-89-D-00037; FTA-TTD-IA-3095

The purpose of this project is to provide the Federal Transit Administration (FTA) with relevant information on existing, and future, stored readable/writable data card technology for fare and toll payment. This project coincides with the FTA's objective of developing a plan for a common standard card-based fare payment system that can be used for **various** public transit **modes**. Information was developed through analyses of existing automated card technology, examination of current and planned applications in relevant transit modes, and numerous in-person interviews with public transit personnel. The key finding was that fare and toll applications have decidedly different requirements. Moreover, the goal of integrating these two applications (person-based and vehicle-based) onto a single card is complicated by a variety of differences, the most significant being the required read range.

?

Ginger Roberts - Search Report

?show files;ds

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S3	2428405	READER? ? OR INTERFACE? ? OR CONTACTS OR SWIPER
S4	22	S1(15N)S2(15N)S3
S5	539	S1 AND S2 AND S3
S6	304	RD (unique items)
S7	860	S1 AND S2
S8	470	RD (unique items)
S9	471	S6 OR S8
S10	12	RD S4 (unique items)

?t10/3,k/all  
>>>KWIC option is not available in file(s): 278

10/3,K/1 (Item 1 from file: 9)  
DIALOG(R)File 9:Business & Industry(R)  
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02652974 02234679  
**SAGEM: Le bibande MC 959**  
(Sagem launches the MC 959 dual-band GSM phone featuring an in-built  
fax/data modem; launches the MC 840M dual-band mobile phone featuring  
smart card readers)  
Journal du Telephone, p 48  
December 1999  
DOCUMENT TYPE: Journal ISSN: 1243-3314 (France)  
LANGUAGE: French RECORD TYPE: Abstract

ABSTRACT:  
...has also launched the MC 840M, a dual-band GSM mobile phone with smart  
card readers for telephony smart cards and banking application  
smart cards . The phone also has a modem for Internet connection.  
Finally, Sagem has launched the DMC 830, a GSM/DECT phone which  
automatically switches between the two modes , weighs 155 g and offers  
hands-free operation. ...

10/3,K/2 (Item 2 from file: 9)  
DIALOG(R)File 9:Business & Industry(R)  
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01753746 (USE FORMAT 7 OR 9 FOR FULLTEXT)  
**Making Contact**  
(SGS-Thomson Microelectronics negotiates with Ramtron International to  
develop a FRAM chip)  
Card Fax, p 2  
February 19, 1997  
DOCUMENT TYPE: Newsletter (United States)  
LANGUAGE: English RECORD TYPE: Fulltext  
WORD COUNT: 95

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:  
...SGS-Thomson's silicon chips. FRAM technology would allow a computer chip  
used on a smart card to operate in both contact and contactless  
interface modes because it uses an internal power source and has expanded  
memory to run separate applications. Current smart card chips that  
operate in two access modes require an outside power source for

activation and additional memory for applications.

...

10/3,K/3 (Item 1 from file: 610)

DIALOG(R)File 610:Business Wire

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00140310 19991116320B1122 (USE FORMAT 7 FOR FULLTEXT)

**Philips Semiconductors Introduces World's First Double Smart Card Interface IC**

Business Wire

Tuesday, November 16, 1999 08:02 EST

JOURNAL CODE: BW LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

DOCUMENT TYPE: NEWSWIRE

WORD COUNT: 577

TEXT:

The TDA8007B Features ISO7816 UART and Supports

**Multi -Protocols** Extending Leadership in the Growing  
Pay TV and Pay Internet Application Markets

Philips Semiconductors today announced the TDA8007B, the world's first double **smart card interface** IC, integrating two analog **interfaces** on one chip for smart protocol management.

For the first time, manufacturers of dual **smart card reader** devices can use one IC to perform authentication and transaction.

The TDA8007B supports **multi -protocol**, asynchronous and synchronous **smart cards** and is the only solution that features an ISO7816 UART for simple implementation. This device further extends Philips Semiconductors advanced family of **smart card interface** ICs to enhance its current leadership in the rapidly growing pay TV and pay Internet...

...waiting time, which are also handled independently from the microcontroller.

Pricing and Availability

The TDA8007B **multi -protocol** double **smart card interface** IC is initially priced at \$3.50 in quantities of 100,000. Samples and production...

10/3,K/4 (Item 2 from file: 610)

DIALOG(R)File 610:Business Wire

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00109648 19990927270B1204 (USE FORMAT 7 FOR FULLTEXT)

**Prototype Testing of Industry's Fastest Smart Card Underway; Litronic's Development of Forte Crypto Card on Target with Customer Demand**

Business Wire

Monday, September 27, 1999 08:41 EDT

JOURNAL CODE: BW LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

DOCUMENT TYPE: NEWSWIRE

WORD COUNT: 624

...on a 32-bit RISC processor and provides an unprecedented level of processing power for **multiple**

applications, algorithms and **protocols** . It is designed with both high speed USB (Universal Serial Bus) and ISO (International Standards Organization) **interfaces** and larger storage capacity and processing speed than existing **smart cards** .

About Litronic's Technology

Litronic's core technology is based on public key infrastructure (PKI...

10/3,K/5 (Item 3 from file: 610)

DIALOG(R)File 610:Business Wire

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00071328 19990708189B0064 (USE FORMAT 7 FOR FULLTEXT)

**Litronic Outlines Plans to Win Part of the Multibillion-Dollar Internet Security Market**

Business Wire

Thursday, July 8, 1999 07:18 EDT

JOURNAL CODE: BW LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

DOCUMENT TYPE: NEWSWIRE

WORD COUNT: 1,679

...Internet access, file access and Web browsers.

These products are bundled with a smart card **reader** /writer and **smart cards** . NetSign Pro offers additional security encryption capabilities.

ProFile manager is a comprehensive PKI life-cycle solution for the management of token-based security systems.

Maestro enables interoperability across **different** security **protocols** simultaneously providing a forward path for legacy security systems and a platform-independent infrastructure for...

10/3,K/6 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2000 The Gale Group. All rts. reserv.

01463674 SUPPLIER NUMBER: 11585124 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Memory, modem PC cards meet new U.S., Japanese standards. (Product Announcement)**

Williams, Tom

Computer Design, v30, n14, p150(1)

Nov, 1991

DOCUMENT TYPE: Product Announcement ISSN: 0010-4566 LANGUAGE:

ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 657 LINE COUNT: 00051

TEXT:

...of plug-in IC cards for laptop, notebook and palmtop computers. The credit-card-size **IC cards** are also expected to find use in portable instrumentation, communication and industrial equipment. The initial offerings are a 2-Mbyte flash memory card and **two modem** cards, along with an **interface** controller IC for interfacing to ISA computer systems. The products support the recently finalized Personal...

10/3,K/7 (Item 1 from file: 621)

DIALOG(R)File 621:Gale Group New Prod. Annou. (R)

(c) 2000 The Gale Group. All rts. reserv.

01687769      Supplier Number: 50222612    (USE FORMAT 7 FOR FULLTEXT)  
**FICS to Present at Robertson Stephens The New Millennium Conference**  
PR Newswire, p0804SFTU001A  
August 4, 1998  
Language: English      Record Type: Fulltext  
Article Type: Article  
Document Type: Newswire; Trade  
Word Count:    436

...      FICS Group's ESD products include electronic banking solutions for Windows-based and Internet Banking, **smart card** -related software and servers.

FICS Group's products are characterized by their multilingual **interfaces**, multicurrency and **multiple** message **protocol** support. The built-in flexibility of our offering means that they can be implemented worldwide...

**10/3,K/8      (Item 1 from file: 636)**  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2000 The Gale Group. All rts. reserv.

02021393      Supplier Number: 43657297    (USE FORMAT 7 FOR FULLTEXT)  
**BULL CP8 FLOURISHES AS THE SMART CARD FINALLY BEGINS TO TAKE OFF**  
Computergram International, n2110, pN/A  
Feb 18, 1993  
Language: English      Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count:    298

(USE FORMAT 7 FOR FULLTEXT)  
TEXT:  
...months, CP8 has signed five new patent licences in Japan for manufacture of cards and **reader** -terminals. CP8 is planning to introduce a **Smart Card** that can be read by terminals that use **different** communications **protocols**. Today, one protocol is used in the US and most of Europe, and another is...

**10/3,K/9      (Item 1 from file: 16)**  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2000 The Gale Group. All rts. reserv.

05674992      Supplier Number: 50152518  
**Low-cost, versatile readers, writers handle array of cards.**  
Hamano, Atsuhiro  
Office Equipment & Products, p34  
May, 1998  
Language: English      Record Type: Abstract  
Article Type: Article  
Document Type: Magazine/Journal; Trade

ABSTRACT:  
Secure-Tech Corp. of Japan has unveiled its ST-100 line of **smart card readers** and writers. The low-cost devices, which are capable of reading **smart cards** that do not have a CPU, are capable of handling **multiple protocols**, making them compatible with a number of **smart cards**. The devices, which get power from an RS-232C **interface**, negating the need for an adapter, uses asynchronous communication provided by the **interface** to control a smart card without the need for another card on a personal computer.  
...

10/3,K/10 (Item 2 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2000 The Gale Group. All rts. reserv.

03339635 Supplier Number: 44621652 (USE FORMAT 7 FOR FULLTEXT)

**Smart Card Vendor Taps LonWorks**

Electronic News (1991), p20

April 25, 1994

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 113

... Neuron IC contains three on chip processors: one processor supports specific applications while the other **two** handle the network **protocol** . The **smart card reader** will **interface** to power lines, RS-232, RF and twisted-pair networks.

10/3,K/11 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2000 The Gale Group. All rts. reserv.

05539533 SUPPLIER NUMBER: 11631889 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Sharp has developed a 32bit notebook PC equipped with a color LCD. (Sharp Corp.) (PCs & Workstations) (Brief Article)**

IDC Japan Report, v17, p81(1)

Oct 31, 1991

DOCUMENT TYPE: Brief Article LANGUAGE: ENGLISH RECORD TYPE:  
FULLTEXT

WORD COUNT: 168 LINE COUNT: 00012

TEXT:

...2.5-inch hard disk drive, a 1.44Mbyte 3.5-inch floppy disk, an **IC card reader** that meets the Japan Electronic Industry Development Association (JEIDA) Version 4.1 standard, and an 8.4-inch amorphous silicon thin-film transistor LCD featuring **two** resolution **modes** , 320-by-200 and 360-by-480. The LCD can display 256 of 260,000...

10/3,K/12 (Item 2 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2000 The Gale Group. All rts. reserv.

03136418 SUPPLIER NUMBER: 05013181 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**DSD computer system improve item tracking. (direct-store delivery system)**

Zimmerman, Susan

Supermarket News, v37, p39(2)

June 22, 1987

ISSN: 0039-5803 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT

WORD COUNT: 1208 LINE COUNT: 00094

... standard are significant to both manufacturers and retailers, he said.

The task force is studying **interface** standards using **two different modes** : a wire or cable connection and a "**smart -card** " device with a memory, which could be carried by truck drivers, Withington said.

He said...

?

?show files;ds

File 348:EUROPEAN PATENTS 1978-1999/DEC W52

(c) 2000 EUROPEAN PATENT OFFICE

Set	Items	Description
S1	19642	IC()CARD? ? OR INTEGRATED()CIRCUIT()CARD? ? OR CHIP()CARD? ? OR SMART()CARD? ? OR INTELLIGENT()CARD? ? OR CARD? ?
S2	849	MC=(T04-K? OR V04-Q02A3? OR W02-C02G7? OR T01-C07C1?) OR I- C=(G06K-019/07:G06K-019/077)
S3	18652	(MULTIPLE OR MULTI? ? OR MORE()THAN()ONE OR SEVERAL OR PLU- RALITY OR TWO OR DIFFERENT OR VARIOUS)(3W)(PROTOCOL? ? OR MOD- E? ?)
S4	115432	READER? ? OR INTERFACE? ? OR CONTACTS OR SWIPER
S5	76	S1(20N)S3(20N)S4
S6	5	S3(20N)S4 AND S2
S7	76	S5(10N)S3(10N)S4
S8	72	S7 NOT S6

?t8/3,k/8,10,14,17,18,25,27,29,31,43,45,62

8/3,K/8

DIALOG(R) File 348:EUROPEAN PATENTS

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01034598

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**Card Reader**

**Kartenleser**

**Lecteur de cartes**

PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD, (216884), 1006, Oaza-Kadoma,  
Kadoma-shi, Osaka 571-0000, (JP), (applicant designated states:  
AT;BE;CH;CY;DE;DK;ES;FI;FR;GB;GR;IE;IT;LI;LU;MC;NL;PT;SE)

INVENTOR:

Kuwamoto, Yoshinobu, 1-25-17-701, Obiyama, Kumamoto-shi, Kumamoto  
862-0924, (JP)  
Egami, Isao, 21-2-205, Matsuki, Tamana-shi, Kumamoto 865-0052, (JP)  
Ipposhi, Koji, 1694, Yamaga,, Yamaga-shi, Kumamoto, 861-0501, (JP)  
Hata, Hidekazu, 1-9-43-312, Kamikumamoto, Kumamoto-shi, Kumamoto,  
860-0843, (JP)  
Tate, Sumio, 4-78-201, Matsugaoka, Kasuga-shi, Fukuoka, 816-0843, (JP)

LEGAL REPRESENTATIVE:

Grunecker, Kinkeldey, Stockmair & Schwanhauser Anwaltssozietat (100721)  
, Maximilianstrasse 58, 80538 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 919945 A2 990602 (Basic)

APPLICATION (CC, No, Date): EP 98122632 981127;

PRIORITY (CC, No, Date): JP 97327415 971128

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: G06K-007/08;

ABSTRACT WORD COUNT: 95

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9922	948
SPEC A	(English)	9922	6132
Total word count - document A			7080
Total word count - document B			0
Total word count - documents A + B			7080

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...SPECIFICATION on the body cover, when communication is completed between the wireless communication means and the **card** inserted in the **card** transfer path 3. A selector switch may be used for the witch means. The selector switch is for selecting between **two modes** designated as, for instance, an AUTO mode and a MANUAL mode, and the owner of the **card reader** can turn the selector switch to the AUTO mode if he desires to discharge the **card** automatically, or to the MANUAL mode if he desires to discharge the card manually. Alternatively...

8/3,K/10

DIALOG(R)File 348:EUROPEAN PATENTS

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01030324

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**MOBILE ELECTRONIC COMMERCE SYSTEM**

**MOBILES ELEKTRONISCHES HANDELSSYSTEM**

**SYSTEME DE COMMERCE ELECTRONIQUE MOBILE**

PATENT ASSIGNEE:

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD, (216884), 1006, Oaza-Kadoma, Kadoma-shi, Osaka 571-0000, (JP), (Applicant designated States: all)

INVENTOR:

TAKAYAMA, Hisashi, 21-22, Matsubara 4-chome, Setagaya-ku, Tokyo 156-0043, (JP)

LEGAL REPRESENTATIVE:

Casalonga, Axel (14511), BUREAU D.A. CASALONGA - JOSSE Morassistrasse 8, 80469 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 950968 A1 991020 (Basic)  
WO 9909502 990225

APPLICATION (CC, No, Date): EP 98937807 980813; WO 98JP3608 980813

PRIORITY (CC, No, Date): JP 97230564 970813

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-017/60

ABSTRACT WORD COUNT: 150

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; Japanese

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9942	17239
SPEC A	(English)	9942	160346
Total word count - document A			177585
Total word count - document B			0
Total word count - documents A + B			177585

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...SPECIFICATION the gate terminal. In addition, at the rear of the gate terminal an RS-232C **interface** is provided for the connection of an external device, such as a gate opening/closing device.

The gate terminal 101 has **two** primary operating **modes** : a ticket examination mode for examining an electronic ticket and a ticket setup mode for...

8/3,K/14

DIALOG(R)File 348:EUROPEAN PATENTS

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00983212

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**Wireless communication device**

**Funkkommunikationsgerat**

**Dispositif de communication sans fil**

**PATENT ASSIGNEE:**

NOKIA MOBILE PHONES LTD., (997966), Keilalahdentie 4, 02150 Espoo, (FI),

(applicant designated states:

AT;BE;CH;CY;DE;DK;ES;FI;FR;GB;GR;IE;IT;LI;LU;MC;NL;PT;SE)

**INVENTOR:**

Knuutila, Jarno, Matti Tapion katu 1 F 17, 33720 Tampere, (FI)

Rauhala, Jyrki, Kusti Salan katu 15, 33720 Tampere, (FI)

Terho, Mikko, Veisunkatu 58 A 1, 33820 Tampere, (FI)

Paaanen, Reijo, Metsamaankatu 7 A, 33820 Tampere, (FI)

**LEGAL REPRESENTATIVE:**

Pursiainen, Timo Pekka (81702), Tampereen Patenttitoimisto Oy,

Hermiankatu 6, 33720 Tampere, (FI)

PATENT (CC, No, Kind, Date): EP 891047 A2 990113 (Basic)

APPLICATION (CC, No, Date): EP 98109153 980520;

PRIORITY (CC, No, Date): FI 972665 970619

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: H04B-001/38;

ABSTRACT WORD COUNT: 182

LANGUAGE (Publication,Procedural,Application): English; English; Finnish

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9902	736
SPEC A	(English)	9902	5546
Total word count - document A			6282
Total word count - document B			0
Total word count - documents A + B			6282

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...SPECIFICATION as random access memory 14a (RAM) and read only memory 14b (ROM/EEPROM). Additionally, the **interface** card 2 comprises a PCMCIA **interface** block 15, **interface** block 16 for the adapter and a digital signal processing unit 17. Thus, the **interface** card 2 primarily comprises those equipment portions which are collective regardless of the data transmission system...

...Consequently, the equipment portion comprises a sufficient quantity of processing and memory capacity for processing **different** data transmission **protocols**, e.g. one or **two** **protocols** simultaneously.

The memory means 14 of the **interface** card contain stored the basic software of the **interface** card for controlling the **interface** card. Into the memory means 14, preferably from the data processing unit 3, are loaded the...

...according to the data transmission system used at the time, these program modules actuating the **interface** card in the selected data transmission system. The program modules contain data e.g. of...

8/3,K/17

DIALOG(R)File 348:EUROPEAN PATENTS

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00945654

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**Method and apparatus for integrated circuit card**

**Verfahren und Gerat fur eine Chipkarte**

**Methode et appareil pour une carte a puce**

**PATENT ASSIGNEE:**

INFO TELECOM, (1418712), Rue de la Foret, B.P. 9, F-67550 Vendenheim,  
(FR), (Applicant designated States: all)

**INVENTOR:**

Copeland, Jeffrey P., 358 Quinapoxet Street,, Jefferson, Massachusetts  
05122, (US)  
Vandenengel, Gerald W., 27 Millbury Street, Grafton, Massachusetts 01519,  
(US)  
Chau, Paul W., 26 Travis Road, Natick, Massachusetts 01760, (US)

**LEGAL REPRESENTATIVE:**

Casalonga, Axel et al (14511), BUREAU D.A. CASALONGA - JOSSE  
Morassistrasse 8, 80469 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 858046 A2 980812 (Basic)  
EP 858046 A3 990908

APPLICATION (CC, No, Date): EP 98300877 980206;

PRIORITY (CC, No, Date): US 37696 P 970207

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;  
MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06K-007/00

ABSTRACT WORD COUNT: 282

**NOTE:**

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9833	2031
SPEC A	(English)	9833	9619
Total word count - document A			11650
Total word count - document B			0
Total word count - documents A + B			11650

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...SPECIFICATION block. All the values for the command block are coded in  
Hex digits.

There are **two modes** of **IC card** (ICC) Command and Response data  
interchange between the portable **reader** device and the host PC. The  
first mode is the pass-through mode. In this...

...CLAIMS tray comprises first and second battery compartments for  
receiving a respective battery therein, and battery **contacts** for  
connecting batteries received in the battery tray to the electronic  
circuitry of the device.

19. The **reader** device according to claim 11, wherein the device has a  
**plurality** of **modes** of operation, including:  
standalone passive mode, wherein the **reader** device is not connected to  
any adapter and simply displays data read from an **IC card** ;  
standalone active mode, wherein the **reader** device is not connected to  
any adapter, accepts and responds to user commands input on...

**8/3,K/18**

DIALOG(R)File 348:EUROPEAN PATENTS

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00939678

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**Electronic transaction processing system and method for operating same**  
**System zum elektronischen Verarbeiten von Transaktionen und Verfahren zum**

**Betreiben desselben**

**Systeme electronique de traitement de transactions et methode pour son fonctionnement**

**PATENT ASSIGNEE:**

Oki Electric Industry Co., Ltd., (225692), 7-12, Toranomon 1-chome  
Minato-ku, Tokyo, (JP), (Applicant designated States: all)

**INVENTOR:**

Mori, Toru, c/o Oki Electric Industries Co., Ltd., 7-12, Toranomon  
1-chome, Minato-ku Tokyo, (JP)  
Saruya, Makoto, c/o Oki Electric Industries Co., Ltd., 7-12, Toranomon  
1-chome, Minato-ku Tokyo, (JP)

**LEGAL REPRESENTATIVE:**

Betten & Resch (101031), Reichenbachstrasse 19, 80469 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 854460 A2 980722 (Basic)

EP 854460 A3 991027

APPLICATION (CC, No, Date): EP 97120994 971128;

PRIORITY (CC, No, Date): JP 96321316 961202

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;  
MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G07F-019/00; G06F-017/60; G07F-007/10

ABSTRACT WORD COUNT: 130

**NOTE:**

Figure number on first page: 2

LANGUAGE (Publication,Procedural,Application): English; English; English

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9830	558
SPEC A	(English)	9830	8068
Total word count - document A			8626
Total word count - document B			0
Total word count - documents A + B			8626

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...SPECIFICATION embodiment of the invention, the automatic teller machine  
1a does not require the first card **reader** /writer 13 and the second card  
reader/writer 15 shown in Fig. 2. However, the...

...1a and transmits digital cash to bank money cards 14a, 14b, 14c. The  
bank money **cards** are of different types, for example, a type A (**Card**  
14a), a type B (**Card** 14b) and a type C (**Card** 14c), each **card** being  
used tor a **different** transaction **mode** because there are a **plurality**  
of transaction **modes** . A **card** array 32 is connected to the server 31  
and is provided with microchip **card** reader/writers 33a, 33b, 33c and  
microchip **card** reader/writers 34a, 34b 34c. The microchip **card**  
**reader** /writers 33a, 33b, 33c have the bank money **cards** 14a 14b, 14c  
prepared for every transaction mode, and write and read information  
stored in these bank money cards. The microchip card **reader** /writers  
34a, 34b, 34c have escrow cards 16a, 16b, 16c corresponding to the bank  
money...

8/3,K/25

DIALOG(R)File 348:EUROPEAN PATENTS

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00892694

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**Dual card smart card reader with visual display**

**Chipkartenleser fur zwei Karten mit visueller Anzeige**

**Lecteur de carte a puce pour deux cartes avec affichage visuel**

PATENT ASSIGNEE:

MOTOROLA, INC., (205770), 1303 East Algonquin Road, Schaumburg, IL 60196,  
(US), (Applicant designated States: all)

INVENTOR:

Jachimowicz, Karen E., RR2 Box 647, Laveen, Arizona 85339, (US)  
Novis, Scott R., 417 W. El Freda Road, Tempe, Arizona 85284, (US)  
Barry, Dennis, 4215 E. Ponca Street, Pheonix, Arizona 85044, (US)

LEGAL REPRESENTATIVE:

Gibson, Sarah Jane et al (73531), Motorola European Intellectual Property  
Operations Midpoint Alencon Link, Basingstoke, Hampshire RG21 7PL, (GB)  
PATENT (CC, No, Kind, Date): EP 817109 A2 980107 (Basic)

EP 817109 A3 991013

APPLICATION (CC, No, Date): EP 97109755 970616;

PRIORITY (CC, No, Date): US 672002 960624

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;  
MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: G06K-007/00

ABSTRACT WORD COUNT: 138

NOTE:

Figure number on first page: 2

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9802	512
SPEC A	(English)	9802	5995
Total word count - document A			6507
Total word count - document B			0
Total word count - documents A + B			6507

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...SPECIFICATION similar to the safety deposit box, a second authorization  
or holder of a second control" **smart** card is required.

Referring now to FIG. 11, illustrated in block diagram is the security  
sequence of the dual card smart card reader 10 of the present invention.  
Dual card **smart** card reader 10 is operational in **several modes**.  
During operation in a first mode, the holder of first **smart** card 13,  
inserts first **smart** card 13 into slot 14 of dual card **smart** card  
reader 10 of the present invention. A security identifier 70 is entered  
to allow dual card smart card **reader** 10 to access/read the information  
contained on first smart card 13. Security identifier 70...

8/3,K/27

DIALOG(R)File 348:EUROPEAN PATENTS

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00882039

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**System for securely accessing data from smart cards**

**System fur gesicherten Zugang zu Chipkartendaten**

**Systeme pour l'accès securise aux donnees de cartes intelligentes**

PATENT ASSIGNEE:

THOMAS DE LA RUE LIMITED, (490914), 6 Agar Street, London WC2N 4DE, (GB),  
(applicant designated states: DE;FR;GB;IT)

INVENTOR:

Lee, Philip S., c/o De La Rue ASI Inc, 1420 K Street NW, Suite 400,  
Washington DC 20005, (US)

LEGAL REPRESENTATIVE:

Skone James, Robert Edmund (50281), GILL JENNINGS & EVERY Broadgate House  
7 Eldon Street, London EC2M 7LH, (GB)

PATENT (CC, No, Kind, Date): EP 807907 A1 971119 (Basic)  
 APPLICATION (CC, No, Date): EP 96303319 960513;  
 PRIORITY (CC, No, Date): EP 96303319 960513  
 DESIGNATED STATES: DE; FR; GB; IT  
 INTERNATIONAL PATENT CLASS: G07F-007/10; G06K-007/06;  
 ABSTRACT WORD COUNT: 91  
 LANGUAGE (Publication,Procedural,Application): English; English; English  
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9711W2	995
SPEC A	(English)	9711W2	4737
Total word count - document A			5732
Total word count - document B			0
Total word count - documents A + B			5732

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...SPECIFICATION card inserted into system 100, then system 100 informs the user that the user's **card** cannot be processed by this system. In another embodiment, the application program can be read in from the inserted **card**, subject to verifications by the security module 120.

CAP1 410 allows application programs to communicate with different types of **cards** using **different protocols** without the need for the application programs to be **card** specific. Application layer 412 is the primary **interface** between the **cards** and the application programs and provides management of the **smart card** environment through simplified industry specific tool sets. The **card** layer 414 provides direct access to the smart card functions. ISO layer 416 controls system...

8/3,K/29

DIALOG(R)File 348:EUROPEAN PATENTS

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00871198

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**Electronic wallet**

**Elektronische Geldbörse**

**Portefeuille électronique**

PATENT ASSIGNEE:

MOTOROLA, INC., (205770), 1303 East Algonquin Road, Schaumburg, IL 60196,  
 (US), (applicant designated states: DE;FR;GB)

INVENTOR:

Lebby, Michael S., 30 N. LaBarge Road, Apache Junction, Arizona 85219,  
 (US)

Lachimowicz, Karen E., RR2, Box 647, Laveen, Arizona 85339, (US)

LEGAL REPRESENTATIVE:

Gibson, Sarah Jane et al (73531), Motorola European Intellectual Property  
 Operations Midpoint Alencon Link, Basingstoke, Hampshire RG21 7PL, (GB)

PATENT (CC, No, Kind, Date): EP 798650 A2 971001 (Basic)

APPLICATION (CC, No, Date): EP 97104660 970319;

PRIORITY (CC, No, Date): US 624285 960329

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-015/02;

ABSTRACT WORD COUNT: 149

LANGUAGE (Publication,Procedural,Application): English; English; English  
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9709W4	757
SPEC A	(English)	9709W4	6358
Total word count - document A			7115

Total word count - document B 0  
Total word count - documents A + B 7115

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...SPECIFICATION into electronic wallet 10, such as a compact disc drive, a floppy-disk drive, a **SMART CARD reader**, or the like, thus enabling a wide variety of data or information to be entered...

...electronic wallet 10. Additionally, it should be understood that input apparatus 26 is used in **several modes** of operation, such as a read mode, a write mode, or a combination of both read and write modes. It is disclosed that in the instance where a **SMART CARD reader** is incorporated into electronic wallet 10, that there is provided a specific space for storage...

8/3,K/31

DIALOG(R)File 348:EUROPEAN PATENTS

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00851808

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**Variably encrypted coded card system**

**Kodiertes Kartensystem mit variabler Verschlüsselung**

**Systeme a carte codee avec un chiffage variable**

PATENT ASSIGNEE:

Nagashio, Kichinosuke, (2216750), 39-2, Ebisu 3-chome, Shibuya-ku, Tokyo 150, (JP), (applicant designated states:  
AT;BE;CH;DE;ES;FR;GB;IT;LI;NL;SE)

INVENTOR:

Nagashio, Kichinosuke, 39-2, Ebisu 3-chome, Shibuya-ku, Tokyo 150, (JP)

LEGAL REPRESENTATIVE:

Meylan, Robert Maurice et al (26161), c/o BUGNION S.A. 10, route de Florissant Case Postale 375, 1211 Geneve 12 - Champel, (CH)

PATENT (CC, No, Kind, Date): EP 785526 A2 970723 (Basic)

APPLICATION (CC, No, Date): EP 97100747 970118;

PRIORITY (CC, No, Date): JP 9623412 960118; JP 9623413 960118

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE

INTERNATIONAL PATENT CLASS: G06K-019/06; G06K-019/10;

ABSTRACT WORD COUNT: 142

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9707W4	1375
SPEC A	(English)	9707W4	6300
Total word count - document A			7675
Total word count - document B			0
Total word count - documents A + B			7675

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...SPECIFICATION be a date code number or a protocol number and the actual protocol for all **cards** issued to that number can be programed into the **card reader**. Fig. 6a illustrates **two** such examples of **protocol** numbers. A nested code 24a is a key code that is read by the simple...

8/3,K/43

DIALOG(R)File 348:EUROPEAN PATENTS

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00598979

ORDER fax of complete patent from Dialog SourceOne. See HELP ORDER 348  
**Nubus dual display card.**

**Doppelte Anzeigesteuerkarte fur Nubus.**

**Double carte de commande d'affichage pour Nubus.**

PATENT ASSIGNEE:

LORAL AEROSPACE CORPORATION, (1369010), 600 Third Avenue, New York, NY  
 10016, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

Youngman, Eric, 161 Danromas Way, San Jose, CA 95129, (US)  
 McReynolds, John, 1638 Orleans Drive, San Jose, CA 95122, (US)  
 Fox, Christopher M., 759 Laurie Avenue, Santa Clara, CA 95054, (US)  
 Blume, David, 1069 Greco Avenue, B136, Sunnyvale 94087, (US)  
 Di Stefano, Charles, 1069 Greco Avenue, B136, Sunnyvale 94087, (US)

LEGAL REPRESENTATIVE:

Vaufrouard, John Charles (50301), Elkington and Fife Prospect House 8  
 Pembroke Road, Sevenoaks, Kent TN13 1XR, (GB)

PATENT (CC, No, Kind, Date): EP 579402 A1 940119 (Basic)

APPLICATION (CC, No, Date): EP 93305018 930628;

PRIORITY (CC, No, Date): US 906509 920630

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-003/14;

ABSTRACT WORD COUNT: 240

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	1067
SPEC A	(English)	EPABF2	1820
Total word count - document A			2887
Total word count - document B			0
Total word count - documents A + B			2887

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...SPECIFICATION address generator manufactured by National Semiconductor,  
 for example. The address decoder 24 may be a **plurality** of **model** 22V10  
 GAL address decoders manufactured by Lattice Semiconductor, Incorporated,  
 for example. The controller 25 may be a **plurality** of **model** 22V10 GAL  
 controllers manufactured by Lattice Semiconductor, for example.  
 Information regarding the design of the Nubus **interface** 21 may be  
 obtained by reading of a book entitled "Designing Nubus **Cards** for the  
 Macintosh", available from Apple Computer, Inc., for example.  
 Furthermore, those skilled in the...

**8/3,K/45**

DIALOG(R)File 348:EUROPEAN PATENTS

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00578979

ORDER fax of complete patent from Dialog SourceOne. See HELP ORDER 348  
**Card, card reader and method for protocol selection.**

**Karte, Kartenleser und Verfahren zur Protokollauswahl.**

**Carte, lecteur de carte et methode de selection d'un protocole.**

PATENT ASSIGNEE:

THOMSON CONSUMER ELECTRONICS S.A., (1090172), 9, Place des Vosges, La  
 Defense 5, F-92400 Courbevoie, (FR), (applicant designated states:  
 AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;LU;MC;NL;PT;SE)

INVENTOR:

Naccache, David, 46 rue St. George, F-94700 Maisons-Alfort, (FR)  
 Fremanteau, Patrice, 30 rue de Carmes, F-67100 Strasbourg, (FR)



Hartnack, Wolfgang, Zilleweg 14a, W-3167 Burgdorf, (DE)  
LEGAL REPRESENTATIVE:  
Einsel, Robert, Dipl.-Ing. (3277), Deutsche Thomson-Brandt GmbH Patent-  
und Lizenzabteilung Göttinger Chaussee 76, D-30453 Hannover, (DE)  
PATENT (CC, No, Kind, Date): EP 583526 A1 940223 (Basic)  
APPLICATION (CC, No, Date): EP 92402313 920820;  
PRIORITY (CC, No, Date): EP 92402313 920820  
DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; MC; NL;  
PT; SE  
INTERNATIONAL PATENT CLASS: G06K-007/00  
ABSTRACT WORD COUNT: 127

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	421
SPEC A	(English)	EPABF2	1069
Total word count - document A			1490
Total word count - document B			0
Total word count - documents A + B			1490

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...ABSTRACT A1

In a lot of cases memory-cards or smart -cards operate with two protocols , whereby the first protocol informs the smart -card reader of the kind of the second protocol.  
If the smart -card reader is equipped with a sequence of light emitting diodes and photoreceptors, and holes (11) coding...

...SPECIFICATION T=0 is a relatively lengthy protocol wherein the card is always slave and the reader is master. A byte is communicated by the sending of 10 bits (a smart bit...

...bit) plus a guard time of two bits between each couple of bytes.

When a card is inserted into a reader (eg. a cash delivery machine, a pay-TV decoder etc), both sides firstly begin to interact according to the protocol T=0. If the card supports a faster T=0 or a different protocol , then both sides switch to the second protocol by the means of a Protocol Type...

8/3,K/62

DIALOG(R)File 348:EUROPEAN PATENTS  
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00345479

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**Method for operating an IC card reader/writer.**

**Verfahren zum Betreiben eines IC-Kartenlesers/-schreibers.**

**Methode d'operation d'un dispositif de lecture/ecriture d'une carte a circuit integre.**

PATENT ASSIGNEE:

Oki Electric Industry Company, Limited, (225690), 7-12, Toranomon 1-chome Minato-ku, Tokyo 105, (JP), (applicant designated states: DE;FR;GB)

INVENTOR:

Takizawa, Toshio, Oki Electric Ind.Co.,Ltd. 7-12, Toranomon 1-chome, Minato-ku Tokyo, (JP)

Hirata, Hiroharu, Oki Electric Ind.Co.,Ltd. 7-12, Toranomon 1-chome, Minato-ku Tokyo, (JP)

LEGAL REPRESENTATIVE:

Betten & Resch (101031), Reichenbachstrasse 19, D-80469 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 347894 A2 891227 (Basic)  
 EP 347894 A3 910724  
 EP 347894 B1 950503  
 APPLICATION (CC, No, Date): EP 89111333 890622;  
 PRIORITY (CC, No, Date): JP 88154620 880624  
 DESIGNATED STATES: DE; FR; GB  
 INTERNATIONAL PATENT CLASS: G06K-007/00;  
 ABSTRACT WORD COUNT: 154

LANGUAGE (Publication,Procedural,Application): English; English; English  
 FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	971
CLAIMS B	(English)	EPAB95	536
CLAIMS B	(German)	EPAB95	441
CLAIMS B	(French)	EPAB95	628
SPEC A	(English)	EPABF1	3473
SPEC B	(English)	EPAB95	3199
Total word count - document A			4444
Total word count - document B			4804
Total word count - documents A + B			9248

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...SPECIFICATION generator scheme stated above is undesirable from the standpoint of applicable range. Specifically, when an **IC card reader** /writer whose clock generator oscillates at a certain fixed clock frequency is loaded with an **IC card** which is operable in a **different frequency mode** , the **reader** /writer cannot read data out of the **IC card** at all so that the processing apparatus is practically useless.

#### SUMMARY OF THE INVENTION

It is therefore an object of the present invention to provide an **IC card reader** /writer which is operable with an IC card with no regard to the frequency mode...

...IC card.

When the frequency mode of the IC card is identifiable, the IC card **reader** /writer determines an operating clock frequency particular to the IC card by using frequency data...

...in the card. Based on the determined operating clock frequency of the IC card, the **reader** /writer produces clock pulses matching the **card** by using reference clock pulses which are generated by clock generators, the clock pulses being fed to the **card** . The **reader** /writer, therefore, is capable of operating with two more different kinds of **IC cards** each having a **different frequency mode** .

Likewise, the **IC card** transactions processing apparatus is usable with **IC cards** the operating clock frequencies of which are different from each other.

#### BRIEF DESCRIPTION OF THE...

...SPECIFICATION generator scheme stated above is undesirable from the standpoint of applicable range. Specifically, when an **IC card** reader/writer whose clock generator oscillates at a certain fixed clock frequency is loaded with an **IC card** which is operable in a **different frequency mode**, the reader/writer cannot read data out of the **IC card** at all so that the processing apparatus is practically useless.

In EP-A-0 237 883 there is described an **IC card** system comprising an **IC card** terminal and an **IC card** which is inserted into the terminal, the terminal including a power-supply circuit for supplying...

...new answer-to-reset signal has an identifiable frequency mode.

Ginger Roberts - Search Report

The method for operating an IC **card reader** /writer apparatus according to the invention is defined in the claims.

The **reader** /writer, therefore, is capable of operating with two different kinds of IC **cards** each having a **different** frequency **mode**

Likewise, the IC **card** transactions processing apparatus is usable with IC **cards** the operating clock frequencies of which are different from each other.

BRIEF DESCRIPTION OF THE...

?

?t6/3,k/all

6/3,K/1

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00778926

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**IC card reader/writer**

**IC Kartenleser/ -schreiber**

**Dispositif pour lire et ecrire sur une carte avec circuit integre**

PATENT ASSIGNEE:

KABUSHIKI KAISHA TOSHIBA, (213137), 72, Horikawa-cho, Saiwai-ku,  
Kawasaki-shi, (JP), (applicant designated states: DE;FR;GB;SE)

INVENTOR:

Toshiyuki, Kawagishi, c/o Intellectual Prop. Div., K.K. Toshiba, 1-1  
Shibaura 1-chome, Minato-ku, Tokyo 105, (JP)

Youko, Kondou, c/o Intellectual Prop. Div., K.K. Toshiba, 1-1 Shibaura  
1-chome, Minato-ku, Tokyo 105, (JP)

LEGAL REPRESENTATIVE:

Blumbach, Kramer & Partner (101302), Patentanwalte Radeckestrasse 43,  
81245 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 727759 A1 960821 (Basic)

APPLICATION (CC, No, Date): EP 96102094 960213;

PRIORITY (CC, No, Date): JP 9530712 950220

DESIGNATED STATES: DE; FR; GB; SE

INTERNATIONAL PATENT CLASS: **G06K-019/07** ; G06K-007/08

ABSTRACT WORD COUNT: 179

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1049
SPEC A	(English)	EPAB96	6152
Total word count - document A			7201
Total word count - document B			0
Total word count - documents A + B			7201

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INTERNATIONAL PATENT CLASS: **G06K-019/07** ...

...ABSTRACT clock must be changed, it changes the operation clock. If the IC card has a **plurality** of operation **modes** , the **reader** /writer (1) supplies a reset signal to the IC card twice when the **reader** /writer (1) selects the operation mode of the IC card, and the contents of initial...

...SPECIFICATION A1

This invention relates to an IC card **reader** /writer for reading out and writing information from or into IC cards having **different protocols** .

For example, an IC card **reader** /writer incorporated into a cash processing device used in a financial organ or the like...

...IC cards of different types of protocols will be used. Therefore, if an IC card **reader** /writer is not designed to cope with the different types of protocols, IC card **readers** /writers respectively designed for the **different** types of **protocols** become necessary and this is inconvenient.

An object of this invention is to provide an IC card **reader** /writer capable of coping with IC cards of **different** types of **protocols** , coping with the function inherent to each protocol and significantly

increasing the convenience thereof.

In...

...object, according to one aspect of the present invention, there is provided an IC card **reader** /writer comprising receiving means for receiving a command containing protocol information corresponding to one of a **plurality** of **protocols** from a host device; analyzing means for analyzing the command received by the receiving means...

...Further, according to another aspect of the present invention, there is provided an IC card **reader** for communication with an IC card capable of coping with a **plurality** of operation **modes**, comprising activating means for activating an IC card by use of a clock with a...

...corresponding protocol according to a command transmitted from the host device to the IC card **reader** /writer, the IC card **reader** /writer can deal with an IC card having a **plurality** of **different protocols**

Further, in a case where the operation clock supplied from the IC card **reader** /writer is changed for an IC card to which a plurality of operation clocks can...an IC card having a plurality of operation clocks.

If the IC card has a **plurality** of operation **modes**, a reset signal is supplied twice to the IC card from the IC card **reader** /writer when the operation mode of the IC card is selected by the IC card...

...other, it is determined that the IC card is an IC card operated in a **plurality** of operation **modes**. The IC card **reader** /writer can easily and stably deal with an IC card having a **plurality** of operation **modes** by setting one of the operation modes.

Further, after the IC card inserted into the IC card **reader** /writer is activated and initial information is transmitted, an IC card node address previously designated...the protocol of the IC card (step S8). When an IC card of a protocol **different** from the **protocol** set in the **reader** /writer 1 is inserted, the protocol is checked in the step S8, and then the...

...initializing the reader/writer. Therefore, in a period after the protocol is set until the **reader** /writer is initialized by the **reader** /writer initialization request command, the **reader** /writer treats a command for a **different protocol** as an abnormal command even if the command is received from the host device 20...

...is previously set according to a command supplied from the host device 20 to the **reader** /writer 1, the **reader** /writer 1 can cope with IC cards having **different protocols**.

Next, the second embodiment is explained with reference to FIG. 5. The second embodiment has a feature that a **reader** /writer 1 selects a protocol used for communication with an IC card according to initial... is effected (steps S5a to S5d). At this time, the clock to be supplied is **different** depending on the **protocol** as described before, and in this example, a clock of 3.5 MHz is first supplied and the **reader** /writer waits for initial information from the IC card.

In a case where the protocol...

...IC card based on the initial information transmitted from the IC card inserted into the **reader** /writer 1 and sets the thus determined protocol, the **reader** /writer 1 can cope with IC cards of **different protocols**. In the second embodiment, a command transmitted from the host device 20 to the **reader** /writer 1 is not changed for each protocol. Therefore, the amount of process effected by...clock of 4.9 MHz, and the IC card is operated in one of the **two modes** when a

reset signal is supplied from the **reader** /writer 1, for example, and the content of the initial information is different depending on...

...Thus, according to the fifth embodiment, in a case where the IC card has a **plurality** of operation **modes**, the **reader** /writer 1 supplies a reset signal to the IC card twice to select the operation...

...the contents of the two initial information items do not coincide with each other, the **reader** /writer 1 determines that the IC card is an IC card of C type operated in a **plurality** of operation **modes**. By setting the operation mode of a higher clock rate, the **reader** /writer 1 can adequately deal with the IC card having a **plurality** of operation **modes**, thereby making it possible to efficiently use the IC card.

Next, the sixth embodiment is...

...processing time can be shortened.

As described above, according to this invention, an IC card **reader** /writer can be provided which can deal with an IC card of a **plurality** of **protocols**, cope with the function inherent to each protocol and significantly increase the convenience thereof. ...

6/3,K/2

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00599166

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**A data communication system.**

**Datenkommunikationssystem.**

**Systeme de communication de donnees.**

PATENT ASSIGNEE:

PLESSEY SEMICONDUCTORS LIMITED, (1442491), Cheney Manor, Swindon,  
Wiltshire SN2 2QW, (GB), (applicant designated states:  
CH;DE;FR;IT;LI;NL)

INVENTOR:

Kitchin, Vaughan Neil, Dr., Sycamore House, High Street, Ingham, Lincoln  
LN1 2YW, (GB)

LEGAL REPRESENTATIVE:

Cockayne, Gillian et al (55531), The General Electric Company plc GEC  
Patent Department Waterhouse Lane, Chelmsford, Essex CM1 2QX, (GB)

PATENT (CC, No, Kind, Date): EP 578457 A2 940112 (Basic)

EP 578457 A3 940817

APPLICATION (CC, No, Date): EP 93305227 930702;

PRIORITY (CC, No, Date): GB 9214308 920706

DESIGNATED STATES: CH; DE; FR; IT; LI; NL

INTERNATIONAL PATENT CLASS: G06K-017/00; G06K-007/00; **G06K-019/073**

ABSTRACT WORD COUNT: 173

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	804
SPEC A	(English)	EPABF2	4707
Total word count - document A			5511
Total word count - document B			0
Total word count - documents A + B			5511

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...INTERNATIONAL PATENT CLASS: **G06K-019/073**

...ABSTRACT on-board micro-processor and/or additional memory to give greater flexibility of operation. The **interface** chip permits **different** communication **protocols** to be utilised and is suitable for different applications by setting it appropriately eg. by...

6/3,K/3

DIALOG(R)File 348:EUROPEAN PATENTS

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00589867

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**Card, card reader and method for protocol selection.**

**Karte, Kartenleser und Verfahren zur Protokollauswahl.**

**Carte, lecteur de carte et methode de selection de protocoles.**

PATENT ASSIGNEE:

THOMSON CONSUMER ELECTRONICS S.A., (1090172), 9, Place des Vosges, La  
Defense 5, F-92400 Courbevoie, (FR), (applicant designated states:  
DE;FR;GB;IT)

INVENTOR:

Naccache, David, 46, rue St. George, F-94700 Maisons-Alfort, (FR)

Fremanteau, Patrice, 30, rue des Carmes, F-67100 Strasbourg, (FR)

Hartnack, Wolfgang, Zilleweg 14a, D-31303 Burgdorf, (DE)

LEGAL REPRESENTATIVE:

Hartnack, Wolfgang, Dipl.-Ing. et al (78102), Deutsche Thomson-Brandt  
GmbH Licensing & Intellectual Property, Gottinger Chaussee 76, 30453  
Hannover, (DE)

PATENT (CC, No, Kind, Date): EP 583723 A1 940223 (Basic)

APPLICATION (CC, No, Date): EP 93112842 930811;

PRIORITY (CC, No, Date): EP 92402313 920820

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06K-007/00; **G06K-019/07**

ABSTRACT WORD COUNT: 127

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF2	421
SPEC A	(English)	EPABF2	1049
Total word count - document A			1470
Total word count - document B			0
Total word count - documents A + B			1470

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...INTERNATIONAL PATENT CLASS: **G06K-019/07**

...ABSTRACT A1

In a lot of cases memory-cards or smart-cards operate with **two** **protocols**, whereby the first protocol informs the smart-card **reader** of the kind of the second protocol.

If the smart-card **reader** is equipped with a sequence of light emitting diodes and photoreceptors, and holes (11) coding...

6/3,K/4

DIALOG(R)File 348:EUROPEAN PATENTS

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00574571

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**Chip card with multi communication protocols**

**Chipkarte mit mehreren Kommunikationsprotokollen**

**Carte a puce a plusieurs protocoles de communication**

**PATENT ASSIGNEE:**

GEMPLUS CARD INTERNATIONAL, (1027402), Avenue du Pic de Bertagne, Parc d'activites de la Plaine de Jouques, F-13420 Gemenos, (FR), (applicant designated states: DE;ES;GB;IT)

**INVENTOR:**

Kowalski, Jacek, Cabinet BALLOT-SCHMIT, 7, rue Le Sueur, F-75116 Paris, (FR)

**LEGAL REPRESENTATIVE:**

Schmit, Christian Norbert Marie et al (60541), Cabinet Ballot-Schmit 7, rue Le Sueur, 75116 Paris, (FR)

PATENT (CC, No, Kind, Date): EP 554164 A1 930804 (Basic)  
EP 554164 B1 970305

APPLICATION (CC, No, Date): EP 93400187 930126;

PRIORITY (CC, No, Date): FR 921001 920130

DESIGNATED STATES: DE; ES; GB; IT

INTERNATIONAL PATENT CLASS: G06K-007/00; **G06K-019/07**

TRANSLATED ABSTRACT WORD COUNT: 146

ABSTRACT WORD COUNT: 150

LANGUAGE (Publication,Procedural,Application): French; French; French

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(French)	EPABF1	398
CLAIMS B	(English)	EPAB97	364
CLAIMS B	(German)	EPAB97	335
CLAIMS B	(French)	EPAB97	374
SPEC A	(French)	EPABF1	4416
SPEC B	(French)	EPAB97	4470
Total word count - document A			4814
Total word count - document B			5543
Total word count - documents A + B			10357

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...INTERNATIONAL PATENT CLASS: **G06K-019/07**

...ABSTRACT Translated)

The invention relates to chip cards.

In order to allow communication with **readers** operating according to **several different** communication **protocols**, it is arranged according to the invention for the card to comprise:

- several conversion circuits...

...into instructions which can be executed by the card, the electrical signals received from the **reader** according to a defined protocol, the various conversion circuits each corresponding to a **different** communication **protocol**,

- and a protocol selection circuit (CNVA, L0, L1, L2, L3, G1 to G6), comprising an...

CLAIMS 1. Chip card capable of communicating with a card **reader** using **several different** communication **protocols**, characterised in that it has :

- several conversion circuits (CNV1, CNV2, CNV3), each conversion circuit being...

...converting into instructions that can be executed by the card electric signals received from the **reader** in compliance with a given protocol, each of the different conversion circuits corresponding to a **different** communication **protocol**,

- and a protocol selection circuit (CNVA, L0, L1, L2, L3, G1 to G6), having an...



6/3,K/5

DIALOG(R) File 348:EUROPEAN PATENTS

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00541169

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**Generation of a clock frequency in a smart card interface**

**Taktfrequenzgeneration in einer Chipkartenschnittstelle**

**Generation d'une frequence d'horloge dans une interface de carte a puce**

PATENT ASSIGNEE:

NOKIA MOBILE PHONES LTD., (997961), P.O. Box 86, 24101 Salo, (FI),  
(Proprietor designated states: all)

INVENTOR:

Lindholm, Rune, Mahlarinne 8, SF-24280 Salo, (FI)

LEGAL REPRESENTATIVE:

Frain, Timothy John (50185), Nokia IPR Department Nokia (UK) Limited  
Summit Avenue Southwood, Farnborough Hampshire GU 14 0NZ, (GB)

PATENT (CC, No, Kind, Date): EP 525963 A2 930203 (Basic)

EP 525963 A3 940615

EP 525963 B1 990818

APPLICATION (CC, No, Date): EP 92305526 920617;

PRIORITY (CC, No, Date): FI 913108 910626

DESIGNATED STATES: DE; FR; GB; SE

INTERNATIONAL PATENT CLASS: G06F-001/08; **G06K-019/07**; G06F-013/42

ABSTRACT WORD COUNT: 119

NOTE:

Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9933	516
CLAIMS B	(German)	9933	408
CLAIMS B	(French)	9933	620
SPEC B	(English)	9933	1873
Total word count - document A			0
Total word count - document B			3417
Total word count - documents A + B			3417

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...INTERNATIONAL PATENT CLASS: **G06K-019/07**

...SPECIFICATION smart card implementation by the GSM recommendation 11.11.

The ISO recommendation 7816-3 defines **various** communication **modes**  
for a smart card **interface**. There is a basic distinction between the  
"Answer to reset" (ATR) mode and normal communication...

?

?show files;ds

File 351:DERWENT WPI 1963-1999/UD=, UM=, & UP=200001

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File 344:Chinese Patents ABS Apr 1985-1999/Dec

(c) 1999 European Patent Office

File 347:JAPIO OCT 1976-1999/SEP(UPDATED 991229)

(c) 1999 JPO & JAPIO

Set	Items	Description
S1	109512	IC()CARD? ? OR INTEGRATED()CIRCUIT()CARD? ? OR CHIP()CARD? ? OR SMART()CARD? ? OR INTELLIGENT()CARD? ? OR CARD? ?
S2	14621	MC=(T04-K? OR V04-Q02A3? OR W02-C02G7? OR T01-C07C1?) OR I-C=(G06K-019/07:G06K-019/077)
S3	20729	(MULTIPLE OR MULTI? ? OR MORE()THAN()ONE OR SEVERAL OR PLURALITY OR TWO OR DIFFERENT OR VARIOUS) (3W) (PROTOCOL? ? OR MODE? ?)
S4	291	S1 AND S3
S5	57	S2 AND S3
S6	4	AU="SARAT J":AU="SARAT J M"
S7	297	S4:S5
S8	143	DC=T AND S7
S9	76	IC=G06K AND S7
S10	170	S8:S9
S11	412648	READER? ? OR INTERFACE? ? OR CONTACTS OR SWIPER
S12	64	S10 AND S11

?t12/3,k/all

12/3,K/1 (Item 1 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012815748 \*\*Image available\*\*

WPI Acc No: 99-621979/199954

XRPX Acc No: N99-458914

Card checking method for re-useable, nontransferable cards

Patent Assignee: SYSTEMS PRODN AG (SYST-N)

Inventor: LOY O; UMUNDUM D

Number of Countries: 025 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 957454	A2	19991117	EP 99109447	A	19990511	G07C-009/00	199954 B
DE 19821013	A1	19991118	DE 1021013	A	19980511	G07C-011/00	200001

Priority Applications (No Type Date): DE 1021013 A 19980511

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 957454	A2			

Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT

LI LT LU LV MC MK NL PT RO SE SI

Language, Pages: EP 957454 (G, 11)

Card checking method for re-useable, nontransferable cards

... Stored data, e.g. period of validity and personal data are read  
 (1). **Card** validity is verified. Images of the **card** user are read from memory. The images are shown by a display (3) for the supervisor to compare with the appearance of the **card** user. If appropriate, fraudulent transfer can be determined. Images of the user may be received by cameras (4) at **card** inspection points during the valid life of the **card**, and stored.

... To check identity of the user of a nontransferable re-useable (e.g. credit) **card** during its validity...

...no possibility of tampering with the recalled image by the user, which can happen with **cards** carrying photographs. **Various** operational **modes** are discussed in the disclosure...

...**card reader** (1  
Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...**T04** ; ...

...**T05**

International Patent Class (Additional): **G06K-007/00**

...Manual Codes (EPI/S-X): **T04-K01**

**12/3,K/2** (Item 2 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012784662 \*\*Image available\*\*

WPI Acc No: 99-590888/199950

XRPX Acc No: N99-435850

**Universal card reader for communicating with cards or badges by electromagnetic linkage**

Patent Assignee: DASSAULT AUTOMATISMES & TELECOM (AVIO ); DASSAULT AT (AVIO )

Inventor: LEVERNE J C; LEVERNE J

Number of Countries: 085 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9946722	A1	19990916	WO 99FR548	A	19990311	G06K-007/08	199950 B
FR 2776148	A1	19990917	FR 993255	A	19990311	H04B-007/22	199950
FR 2776147	A1	19990917	FR 983215	A	19980311	H04B-007/22	199950

Priority Applications (No Type Date): FR 983215 A 19980311

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
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WO 9946722	A1			
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Designated States (National): AE AL AM AT AU AZ BA BB BG BR BY CA CH CN  
CU CZ DE DK EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ  
LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK  
SL TJ TM TR TT UA UG US UZ VN YU ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR  
IE IT KE LS LU MC MW NL OA PT SD SE SL SZ UG ZW

Language, Pages: WO 9946722 (F, 26)

**Universal card reader for communicating with cards or badges by electromagnetic linkage**

... The system is able to communicate with **multiple** circuits of **different protocol** type.

... The universal **reader** (1) searches for (2,11,12,17,18, 19) the nomadic objects (7) by emitting...

...31,32,33,35) an echo signal according to its own particular protocol.

The universal **reader** (1) analyses (11,12) each echo signal and deduces from it the specific protocol applied by the nomadic object (7). The universal **reader** (1) inserts (11,12) the messages to be transmitted into the **reader** frames, in conformity with the specific protocol, so that they may be interpreted by the...

...Transfer of data between universal **reader** and selection of nomadic

objects using varied protocols...  
...Enables single **reader** to cooperate with number of **cards** or badges  
without physical contact...  
...The figure shows the **reader** with transmission and reception aerials...  
...Title Terms: **CARD** ;  
Derwent Class: **T04** ;  
International Patent Class (Main): **G06K-007/08** ...  
Manual Codes (EPI/S-X): **T04-K02** ...  
...**W02-C02G7**

**12/3,K/3** (Item 3 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

012774284 \*\*Image available\*\*  
WPI Acc No: 99-580511/199949  
XRPX Acc No: N99-428563  
· Multi- protocol smart card **system for various applications such as  
electronic commerce, security access control and health card record  
maintenance**  
Patent Assignee: GEMPLUS SCA (GEMP-N)  
Inventor: SARAT J M  
Number of Countries: 082 Number of Patents: 001  
Patent Family:  
Patent No Kind Date Applicat No Kind Date Main IPC Week  
WO 9949415 A2 19990930 WO 99EP2051 A 19990317 G06K-019/00 199949 B  
Priority Applications (No Type Date): US 9848010 A 19980326; US 9848009 A  
19980326  
Filing Details:  
Patent Kind Filing Notes Application Patent  
WO 9949415 A2  
Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU  
CZ DE DK EE ES FI GB GE GH GM HU ID IL IN IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT UA UG UZ VN YU ZA ZW  
Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR  
IE IT KE LS LU MC MW NL OA PT SD SE SL SZ UG ZW  
Language, Pages: WO 9949415 (E, 27)  
Multi- protocol smart card **system for various applications such as  
electronic commerce, security access control and health card record  
maintenance**

... The user **card** (18) contains a microprocessor (24), a memory  
(22) and **contacts** (20) for transferring data to and from the  
microprocessor and memory. One set of **contacts** are associated with a  
set of signal that confirm to a ISO protocol and other...  
... A **reader** (16) receives the user **card** which includes several  
**contacts** (26) and a mode signal generator. The set of mating **contacts**  
in the **interface** device, corresponds to the first set of **contacts**  
in the user **card** . A mode contact corresponds to other contact of the  
user **card** . The generator provides a signal at the mode contact which  
causes the microprocessor to operate in accordance with the non-ISO  
protocol mode when the user **card** is received in the **interface**  
device. INDEPENDENT CLAIMS are also included for the following...  
...a) user **card** transaction system...  
...b) method for selectively operating microprocessor in one of **plurality**

of **modes**

...

...For electronic commerce, security access control, health **card** record maintenance...

...The **card** that is capable of communicating with a variety of external devices using **different protocols** . Does not require a relatively expensive **reader** to access the information and/or functionality present in the **card** . The **card** is compatible with **several different protocols** .

...

...The figure shows the plan view of a **smart card** and a **reader** .

...

...**Reader** (16...

...**Card** (18...

...**Contacts** (20,26

...Title Terms: **CARD** ;

Derwent Class: **T01** ; ...

...**T04**

International Patent Class (Main): **G06K-019/00**

Manual Codes (EPI/S-X): **T01-C07C1** ...

...**T04-K02**

**12/3,K/4** (Item 4 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012658675 \*\*Image available\*\*

WPI Acc No: 99-464780/199939

XRFX Acc No: N99-348443

**High speed processing system of network interface - has network interface card which connects network and peripheral equipments which perform predetermined partial priority protocol process**

Patent Assignee: RICOH KK (RICO )

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
JP 11196116	A	19990721	JP 9812107	A	19980105	H04L-012/40	199939	B

Priority Applications (No Type Date): JP 9812107 A 19980105

Language, Pages: JP 11196116 (5)

**High speed processing system of network interface - ...**

...**has network interface card which connects network and peripheral equipments which perform predetermined partial priority protocol process**

...Abstract (Basic): NOVELTY - The peripheral device (3) is connected to a network (1) via a network **interface card** (4) which performs a predetermined priority protocol process while the network **interface card** performs other protocol process. The priority protocol is downloaded and changed to a peripheral device side depending on necessity of the network **interface card** .

...

...USE - Used in local area network of **multi -protocol** environment involving copier, facsimile, image scanner, digital camera...

...used frequently and it is also made to perform other protocol processes by the network **interface card** and optimum speed improvement in network is obtained. DESCRIPTION OF DRAWING(S) - The figure shows the high speed process system for LAN. (1) Network; (3) Peripheral device; (4) Network **interface card** .

...Title Terms: **INTERFACE** ;  
Derwent Class: **T01** ;

**12/3,K/5** (Item 5 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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012652052 \*\*Image available\*\*  
WPI Acc No: 99-458157/199938  
XRPX Acc No: N99-342706

**Customized antenna interface for a smart card operating in contactless mode**

Patent Assignee: ON TRACK INNOVATIONS LTD (ONTR-N)

Inventor: ADUK M; BASHAN O; GILBOA R; ITAY N

Number of Countries: 084 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
WO 9934326	A1	19990708	WO 98IL624	A	19981228	G06K-019/07	199938 B	
AU 9915761	A	19990719	AU 9915761	A	19981228	G06K-019/07	199951	

Priority Applications (No Type Date): IL 122841 A 19971231

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
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WO 9934326	A1			
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Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG ZW

AU 9915761	A	Based on	WO 9934326
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Language, Pages: WO 9934326 (E, 56)

**Customized antenna interface for a smart card operating in contactless mode**

...G06K-019/07

... A contact/contactless **smart card** includes an antenna **interface** (16) for contactless data transmission between the **smart card** and a remote transceiver. A variable loading device (45) produces a change in the impedance...

...to allow amplitude and/or phase modulation of a radio frequency carrier. Hence, the antenna **interface** can be customized, by means of the variable loading device, for different applications.

... For a **smart card** operating in contactless mode...

...Can operate in accordance with **different** communication **protocols** .

...

...The figure shows an antenna **interface** that can be customized for different applications...

...Antenna **interface** (16  
...Title Terms: **INTERFACE** ;  
Derwent Class: **T04** ;  
International Patent Class (Main): **G06K-019/07**  
Manual Codes (EPI/S-X): **T04-K01** ...

...W02-C02G7

**12/3,K/6** (Item 6 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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012637436 \*\*Image available\*\*  
WPI Acc No: 99-443540/199937  
XRPX Acc No: N99-330815

**Protocol converter for data processing system**  
Patent Assignee: INT BUSINESS MACHINES CORP (IBMC )  
Inventor: BILLHEIMER E A; SCHUSTER R W; WALKER J E  
Number of Countries: 001 Number of Patents: 001  
Patent Family:  
Patent No Kind Date Applicat No Kind Date Main IPC Week  
US 5931933 A 19990803 US 97877014 A 19970616 G06F-013/40 199937 B

Priority Applications (No Type Date): US 97877014 A 19970616  
Language, Pages: US 5931933 (5)

... Several logic circuits (122,124,126,128) operate in **different protocols** . A bus multiplexer and translator (130) translates protocols to an industry standard architecture (ISA) type...  
... The protocols include microchannel architecture (MCA) protocol, personal computer memory **card** industry association (PCMCIA) protocol and peripheral component **interface** (PCI) protocol. Buses (210,310) connect external devices (200,300) to the converting chip...  
Derwent Class: **T01**

**12/3,K/7** (Item 7 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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012608124 \*\*Image available\*\*  
WPI Acc No: 99-414228/199935  
XRPX Acc No: N99-310321

**PCMCIA peripheral controller for notebook computer - has PCMCIA card is used as input-output device and peripheral device, in two different operation modes, using PGFA code**  
Patent Assignee: TOSHIBA KK (TOKE ); TOSHIBA AMERICA INFORMATION SYSTEMS INC (TOKE )  
Inventor: KOO J; KOU J  
Number of Countries: 002 Number of Patents: 002  
Patent Family:  
Patent No Kind Date Applicat No Kind Date Main IPC Week  
JP 11167544 A 19990622 JP 98226087 A 19980810 G06F-013/12 199935 B  
US 5978862 A 19991102 US 97907509 A 19970808 G06F-013/10 199953

Priority Applications (No Type Date): US 97907509 A 19970808  
Language, Pages: JP 11167544 (11)

... **has PCMCIA card is used as input-output device and peripheral device, in two different operation modes, using PGFA code**

...Abstract (Basic): NOVELTY - A PGFA code of PCMCIA **card** is loaded, via PCMCIA **interface**, with memory write-in information from host computer (101). Then, **card** is reset so that **card** can be operated as input-output device using newly loaded PGFA code in one mode. In another mode, the **card** is used as a peripheral device using PGFA code. DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for PCMCIA **card** programming procedure...

...ADVANTAGE - Dynamically sets PCMCIA compatible **card** which can load corrected setting parameter. Host computer interacts with function code which can be...

...device and downloaded newly. DESCRIPTION OF DRAWING(S) - The figure shows system diagram of PCMCIA **card interface**. (101) Host computer

...Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...T04  
International Patent Class (Additional): **G06K-019/00** ...

...G06K-019/077  
...Manual Codes (EPI/S-X): **T04-K**

12/3,K/8 (Item 8 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

012577657 \*\*Image available\*\*  
WPI Acc No: 99-383764/199932  
XRPX Acc No: N99-287267  
Multiple protocols **implemented communication data routing method using cross bar switches in digital telecommunication**  
Patent Assignee: ADVANCED MICRO DEVICES INC (ADMI )  
Inventor: HARTMANN A; WAKELAND C  
Number of Countries: 001 Number of Patents: 001  
Patent Family:  
Patent No Kind Date Applicat No Kind Date Main IPC Week  
US 5905873 A 19990518 US 97783887 A 19970116 G06F-013/00 199932 B

Priority Applications (No Type Date): US 97783887 A 19970116  
Language, Pages: US 5905873 (24)

Multiple protocols **implemented communication data routing method using cross bar switches in digital telecommunication**

... HDLC, ISDN, Lap B, ATM, X.25, frame relay, digital data service, fiber distributed data **interface** (FDDI), TI, HFC and DSL. An INDEPENDENT CLAIM is also included for a communication system...

Technology Focus:

... The PC **cards** used satisfy personal computer memory **card** internal association (PCMCIA) standard.

Derwent Class: **T01** ;

12/3,K/9 (Item 9 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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012457727 \*\*Image available\*\*  
WPI Acc No: 99-263835/199922



XRPX Acc No: N99-196538

**User communication device e.g. for messaging application for transmitting and receiving email**

Patent Assignee: ERICSSON INC (TELF )

Inventor: BANKLER B; BARATT M A; MOON B G; WOOLDRIDGE T A

Number of Countries: 081 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
WO 9917504	A1	19990408	WO 98US18795	A	19980909	H04L-012/58	199922	B
AU 9893821	A	19990423	AU 9893821	A	19980909	H04L-012/58	199935	

Priority Applications (No Type Date): US 97940138 A 19970929

Filing Details:

Patent	Kind	Filing	Notes	Application	Patent
WO 9917504	A1				

Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG ZW

AU 9893821 A Based on WO 9917504

Language, Pages: WO 9917504 (E, 36)

... A mail)source module operatively associated with the mail provider module receives electronic messages including **several** mail source **protocols** each associated with a different mail service provider.

... sink module is operatively associated with the mail provider module to transmit electronic messages including **several** mail sink **protocols** each associated with a different mail service provider. The mail sink module selects one of...

...Interfaces with several mail servers utilized by different mail service providers during receipt and transmission of...

...message. Provides user communications device with ability to receive and sends business cars including business **card** agents

Derwent Class: T01 ;

12/3,K/10 (Item 10 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012413825 \*\*Image available\*\*

WPI Acc No: 99-219933/199919

XRPX Acc No: N99-162771

**Prepaid type energy meter for energy supply of different modes - has card write-in device that records of frequency transducer, which converts frequency corresponding to computed total amount of energy into read frequency of card reader, in inserted prepaid card**

Patent Assignee: YAZAKI CORP (YAZA )

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
JP 11053635	A	19990226	JP 97212262	A	19970806	G07F-015/06	199919	B

Priority Applications (No Type Date): JP 97212262 A 19970806

Language, Pages: JP 11053635 (6)

**Prepaid type energy meter for energy supply of different modes - ...**

...has card write-in device that records of frequency transducer, which converts frequency corresponding to computed total amount of energy into read frequency of card reader, in inserted prepaid card

...Abstract (Basic): NOVELTY - A card write-in device (29) records the output of a frequency transducer (21), which performs the...

...frequency corresponding to the computed total amount of energy into the read frequency of a card reader (27), in an inserted prepaid card (5). DETAILED DESCRIPTION - A prepaid type energy meter (1) includes a memory (13) which stores...

...energies that are measured and converted based on the data contents of the memory. A card reader reads the predetermined frequency from a prepaid card corresponding to a recorded fee...

...USE - For energy supply of different modes . Applicable for measuring amount of used gas e.g. liquefied petroleum gas (LPG) used at...

...ADVANTAGE - Attains reduction of work load of consumer since energy supply of different modes is enabled using one card . Optimum energy can be selected since comparison examination of each amount of common energy can...

...block diagram of a prepaid type energy meter. (1) Prepaid type energy meter; (5) Prepaid card ; (11) Energy amount transducer; (13) Memory; (21) Frequency transducer; (27) Card reader ; (29) Card write-in device...

...Title Terms: CARD ;

Derwent Class: T05

12/3,K/11 (Item 11 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012301254 \*\*Image available\*\*

WPI Acc No: 99-107360/199910

XRPX Acc No: N99-077577

Data transmission arrangement - comprises modem arranged at interface of telephone subscriber lines in connection area of telecommunications exchange system, which operates according to same modem protocol as participating subscriber site

Patent Assignee: SIEMENS AG (SIEI )

Inventor: AHRNDT T; BINDE S; BRAUN M; HILSCHER I; KLOPPE K; RUDOLF H

Number of Countries: 003 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
DE 19750931	A1	19990128	DE 1050931	A	19971117	H04M-011/06		199910 B
CN 1202063	A	19981216	CN 98109321	A	19980527	H04L-027/00		199918
CA 2238509	A	19981127	CA 2238509	A	19980525	H04M-011/06		199919

Priority Applications (No Type Date): DE 1022155 A 19970527

Language, Pages: DE 19750931 (6)

... comprises modem arranged at interface of telephone subscriber lines in connection area of telecommunications exchange system, which operates according to...

...Abstract (Basic): lines to a telecommunications exchange system. A modem (MODCh1, MODCh2) is respectively arranged at the interfaces of the

affected telephone subscriber lines (TL1, TL2) in the connection area of the telecommunications...

...The modems are preferably placed as close as possible at the **interface** for the telephone subscriber line, so that the data signals transmitted on this line pass...

...transmission at high bit-rates. The modems are preferably arranged to operate alternatively according to **different modem protocols**.

...

...USE - For PC telephone line **interface card**.

...Title Terms: **INTERFACE** ;  
Derwent Class: **T01** ;

**12/3,K/12** (Item 12 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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012240081 \*\*Image available\*\*  
WPI Acc No: 99-046189/199904  
XRPX Acc No: N99-033689

**Control apparatus for computer networking and for allowing shared access for two computer systems to network via interface device - has two network protocol providers executing on respective computer systems and both sharing same network interface card on second computer system for interconnecting both computers, and router**

Patent Assignee: UNISYS CORP (BURS )  
Inventor: DISNEY W W; EBERSOLE D E; JOHNSON R A  
Number of Countries: 020 Number of Patents: 001  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9856150	A1	19981210	WO 98US11201	A	19980601	H04L-029/06	199904 B

Priority Applications (No Type Date): US 9748723 A 19970602

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9856150	A1			

Designated States (National): BR DE GB JP

Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LU

MC NL PT SE

Language, Pages: WO 9856150 (E, 64)

... **for computer networking and for allowing shared access for two computer systems to network via interface device...**

...**has two network protocol providers executing on respective computer systems and both sharing same network interface card on second computer system for interconnecting both computers, and router**

...Abstract (Basic): and having a second network address to both access a network via a same network **interface card** installed on the second system. An interconnection is provided between an input/output device (I...

...A router executes on the second computer system that passes data received by the network **interface card** to the first network protocol provider via the interconnection when a network address received with...

...both the first and second network protocol providers to the network via the same network **interface card** in a manner transparent to both protocol providers...

...ADVANTAGE - Avoids need to develop network **interface cards** for proprietary system as new network protocols and standards evolve.

**Interface cards** are designed for open system...

...Title Terms: **INTERFACE** ;

Derwent Class: **T01** ;

12/3,K/13 (Item 13 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012237840 \*\*Image available\*\*

WPI Acc No: 99-043947/199904

XRFX Acc No: N99-031997

**Network protocol analyser and analysis method - includes network card information setting device and protocol information unit, filter unit and interpretation unit**

Patent Assignee: INST INFORMATION IND (INFO-N)

Inventor: LIN C

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
TW 341679	A	19981001	TW 97109186	A	19970630	G06F-015/163	199904 B

Priority Applications (No Type Date): TW 97109186 A 19970630

Language, Pages: TW 341679 (16)

... **includes network card information setting device and protocol information unit, filter unit and interpretation unit**

...Abstract (Basic): 1) Protocol information unit; for storing the filter descriptions files and interpretation description files of **several network protocols** to form a filter and interpretation database...

...2) Network **card** information setting device; for setting information according to the network protocol type of the **interface card** connected to one of the network **interfaces** and detected by the computer system...

...3) Protocol filter unit; for receiving a network protocol original packet of the network protocol **interface card** and generating a network protocol filter packet, based on the information of the network **card** information setting device and the filter database of the network protocol information unit...

...unit, and generating a network protocol interpretation packet, based on the information of the network **card** information setting device and the interpretation database of the network protocol information unit...

...USE - In network **interface** unit of computer system supporting Plug and Play functions; for interpreting packets of **different network protocol** types...

...Title Terms: **CARD** ;

Derwent Class: **T01** ;

12/3,K/14 (Item 14 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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012203905 \*\*Image available\*\*

WPI Acc No: 99-010011/199901

Related WPI Acc No: 98-467817; 99-010012; 99-010016; 99-010017; 99-010018;  
99-010019; 99-010020; 99-010021

XRPX Acc No: N99-007292

Integrated circuit card interface **command responding -  
transmitting command from interface to integrated circuit card  
under transmission protocol requiring card to have prior information  
related to data, if any, to be transferred**

Patent Assignee: MONDEX INT LTD (MOND-N)

Inventor: PEACHAM D A; RICHARDS T P

Number of Countries: 081 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9852152	A2	19981119	WO 98GB1401	A	19980514	G06K-019/07	199901 B
AU 9877771	A	19981208	AU 9877771	A	19980514	G06K-019/07	199916

Priority Applications (No Type Date): US 9878051 A 19980513; US 9746514 A  
19970515; US 9746543 A 19970515

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9852152	A2			

Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU  
CZ DE DK EE ES FI GB GE GH GM GW HU ID IL IS JP KE KG KP KR KZ LC LK LR  
LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM  
TR TT UA UG UZ VN YU ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR  
IE IT KE LS LU MC MW NL OA PT SD SE SZ UG ZW

AU 9877771 A Based on WO 9852152

Language, Pages: WO 9852152 (E, 114)

Integrated circuit card interface **command responding...**

**...transmitting command from interface to integrated circuit card  
under transmission protocol requiring card to have prior information  
related to data, if any, to be transferred**  
...G06K-019/07

...Abstract (Basic): The method involves selecting an expected case for a  
command representing transferred between the **intérface** and the  
**integrated circuit card**. Further it requires determining whether  
the expected case is applicable to the processing the command if the  
expected case is applicable. The command is transmitted from the  
**interface** to the **integrated circuit card** under a transmission  
protocol requiring the **integrated circuit card** to have prior  
information related to the data, if any, to be transferred...

...The prior information is related to the direction of the data to be  
transferred. The **integrated circuit card** supports **several**  
transmission **protocols**.

...Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...**T04**  
International Patent Class (Main): **G06K-019/07**  
Manual Codes (EPI/S-X): **T01-C07C1** ...

...**T04-K02**

12/3,K/15 (Item 15 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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011971449 \*\*Image available\*\*

WPI Acc No: 98-388359/199833

XRPX Acc No: N98-302768

**Contact and contactless operation modes for transaction card - has card with semiconductor device for handling transactions that may be interfaced by contacts or contactless links**

Patent Assignee: ON TRACK INNOVATIONS LTD (ONTR-N)

Inventor: BASHAN O; ADUK M; GILBOA R; ITAY N

Number of Countries: 081 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9829830	A1	19980709	WO 97IL436	A	19971229	G06K-019/07	199833 B
AU 9878930	A	19980731	AU 9878930	A	19971229	G06K-019/07	199849

Priority Applications (No Type Date): IL 119943 A 19961231

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
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WO 9829830	A1			
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Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE GH GM GW HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU ZW

Designated States (Regional): AT BE CH DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG ZW

AU 9878930	A	Based on	WO 9829830
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Language, Pages: WO 9829830 (E, 45)

**Contact and contactless operation modes for transaction card - ...**

**...has card with semiconductor device for handling transactions that may be interfaced by contacts or contactless links**  
**...G06K-019/07**

...Abstract (Basic): The transaction **card** , e.g. credit **card** , includes an electronic circuit and interfacing elements. The **card** has a microprocessor (14) that handles the transactions in the appropriate protocol. A set of **contacts** (11) are provided in a standard location to allow communication with the processor by standard...

...protocols. A coil antenna (15) is positioned to detect contactless data communications according to a **different protocol** .

...

...The coil has an **interface** circuit (16) that converts contactless data streams into a form consistent with the contact inputs...

...ADVANTAGE - Extends applications for which **card** can be used by handling both contact and contactless protocols

...Title Terms: **CARD** ;

Derwent Class: **T04** ;

International Patent Class (Main): **G06K-019/07**

Manual Codes (EPI/S-X): **T04-K** ...

**...W02-C02G7**

12/3,K/16 (Item 16 from file: 351)

DIALOG(R)File 351:DERWENT WPI

(c) 2000 DERWENT INFO LTD. All rts. reserv.

011948384    \*\*Image available\*\*  
WPI Acc No: 98-365294/199832  
Related WPI Acc No: 97-438145  
XRPX Acc No: N98-285326

**Twin loop dual oscillation mode antenna configuration for smart card reading system - has oscillator drive circuit mounted between antenna conductors in laminated smart chip card, whose oscillating frequency is strongly dependent on radiation resistance changes of antenna**

Patent Assignee: HALPERN J W (HALP-I)

Inventor: HALPERN J W

Number of Countries: 001    Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
GB 2321551	A	19980729	GB 9627120	A	19961231	G06K-007/08	199832 B

Priority Applications (No Type Date): GB 969102 A 19960501; GB 965050 A 19960322; GB 966764 A 19960329

Language, Pages: GB 2321551 (11)

**Twin loop dual oscillation mode antenna configuration for smart card reading system...**

**...has oscillator drive circuit mounted between antenna conductors in laminated smart chip card, whose oscillating frequency is strongly dependent on radiation resistance changes of antenna**

**...Abstract (Basic): The antenna configuration oscillates in two modes in which the antenna loops respectively oscillate in phase opposition and oscillate linearly in unison...**

**...Electromagnetic energy is transferred from a card transaction terminal to a chip card incorporating layers of bonded laminates carrying the antenna consisting of a middle branch and two...**

**...may optionally be interrupted for the insertion of a capacitive or inductive link element. The reader antenna has broadly the same size and shape, and is connected to a transmitter drive...**

**...oscillating frequency is strongly co-determined by the mutual inductance and coupling conditions of the card antenna and reader antenna when brought close to each other...**

**...Title Terms: CARD ;**

**...Derwent Class: T04 ;**

**International Patent Class (Main): G06K-007/08**

**Manual Codes (EPI/S-X): T04-K02 ...**

**...W02-C02G7**

**12/3,K/17    (Item 17 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

(c) 2000 DERWENT INFO LTD. All rts. reserv.

011908018    \*\*Image available\*\*  
WPI Acc No: 98-324928/199829  
XRPX Acc No: N98-254153

**Expansion card interface for portable data processors - has expansion card with standard normal operation mode and one or several special operation modes that differ from standard, operation mode is arranged to be selected by mode-selection line**

Patent Assignee: NOKIA MOBILE PHONES LTD (OYNO )

Inventor: KOEPPAE V

Number of Countries: 024    Number of Patents: 002

## Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 849683	A2	19980624	EP 97203718	A	19971127	G06F-013/38	199829 B
FI 9605107	A	19980620	FI 965107	A	19961219	G06F-013/38	199838

Priority Applications (No Type Date): FI 965107 A 19961219

## Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 849683	A2			

Designated States (Regional):

AL AT BE CH DE DK ES FI FR GB GR IE IT LI

LT LU LV MC MK NL PT RO SE SI

Language, Pages: EP 849683 (E, 14)

**Expansion card interface for portable data processors...**

**...has expansion card with standard normal operation mode and one or several special operation modes that differ from standard, operation mode is arranged to be selected by mode-selection line**

**...Abstract (Basic):** The device includes an expansion-card connector (6b) for connecting the expansion card (1) to an expansion-card connector (6a) of an electronic device (2). The expansion card has a standard normal operation mode and one or **several** special operation **modes** that differ from the standard. The operation mode is arranged to be selected by at...

**...line (3).** The mode-selection line is combined to a contact pin of the expansion-card connector (6b) which contact pin has a respective contact pin for the expansion-card connector of the electronic device. The respective contact pin is defined as the input line...

...Title Terms: **CARD** ;Derwent Class: **T01****12/3,K/18 (Item 18 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

(c) 2000 DERWENT INFO LTD. All rts. reserv.

011728900 \*\*Image available\*\*

WPI Acc No: 98-145810/199813

XRPX Acc No: N98-115338

**Integrated circuit with dual connection modes - comprises contact pads for direct electrical connection and aerial element for contactless connection**

Patent Assignee: SOLAIC SA (SOLA-N); SCHLUMBERGER SYSTEMS (SLMB );

SCHLUMBERGER SYSTEMES (SLMB )

Inventor: BILLEBAUD P; BITSCHNAU T; FLETOUT C; THEVENOT B

Number of Countries: 020 Number of Patents: 006

## Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9806063	A1	19980212	WO 97FR1434	A	19970731	G06K-019/077	199813 B
FR 2752077	A1	19980206	FR 969802	A	19960802	G06K-019/077	199813
FR 2753819	A1	19980327	FR 9611488	A	19960920	G06K-019/077	199819
AU 9739445	A	19980225	AU 9739445	A	19970731	G06K-019/077	199829
EP 917688	A1	19990526	EP 97936722	A	19970731	G06K-019/077	199925
			WO 97FR1434	A	19970731		
CN 1226986	A	19990825	CN 97196976	A	19970731	G06K-019/077	199952

Priority Applications (No Type Date): FR 9611488 A 19960920; FR 969802 A 19960802

## Filing Details:

Patent	Kind	Filing Notes	Application	Patent



WO 9806063 A1

Designated States (National): AU CN US

Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LU MC  
NL PT SE

AU 9739445 A Based on WO 9806063

EP 917688 A1 Based on WO 9806063

Designated States (Regional): AT BE DE ES FR GB

Language, Pages: WO 9806063 (F, 28); FR 2752077 (12); EP 917688 (F)

...G06K-019/077

...Abstract (Basic): The integrated circuit offers **two modes** of connection and includes a contactless connection element consisting of an aerial (1) carried by...

...connected to an integrated circuit (6) by wires (7) to provide a connection with the **contacts**. Further conductive regions (18) are connected to the integrated circuit by wires (19) to ensure connection with a contactless **interface** included within the integrated circuit

...  
...USE - For **IC card**.  
...

...ADVANTAGE - Single circuit offers **two** connection **modes** for communications with integrated circuit module

Derwent Class: **T04** ;International Patent Class (Main): **G06K-019/077**Manual Codes (EPI/S-X): **T04-K01** ...

...V04-Q02A3

**12/3,K/19 (Item 19 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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011716266 \*\*Image available\*\*

WPI Acc No: 98-133176/199813

XRPX Acc No: N98-105232

**Electric supply for mixed function microcircuit with or without contacts - senses presence of inductively coupled alternating supply to operate switch which disconnects external supply contact for other operating mode**

Patent Assignee: INSIDE TECHNOLOGIES (INSI-N); INSIDE TECHNOLOGIES SA (INSI-N)

Inventor: KOWALSKI J

Number of Countries: 079 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
FR 2752076	A1	19980206	FR 9610032	A	19960805	G06K-019/07	199813 B
WO 9806057	A1	19980212	WO 97FR1230	A	19970708	G06K-007/00	199813
AU 9736244	A	19980225	AU 9736244	A	19970708	G06K-007/00	199829
EP 917684	A1	19990526	EP 97932854	A	19970708	G06K-007/00	199925
			WO 97FR1230	A	19970708		
CN 1227646	A	19990901	CN 97197053	A	19970708	G06K-007/00	199953

Priority Applications (No Type Date): FR 9610032 A 19960805

Filing Details:

Patent Kind Filing Notes Application Patent

WO 9806057 A1

Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU  
CZ DE DK EE ES FI GB GE HU IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV  
MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG  
US UZ VN YU

Designated States (Regional): AT BE CH DE DK EA ES FI FR GB GH GR IE IT  
KE LS LU MC MW NL OA PT SD SE SZ UG ZW

AU 9736244 A Based on WO 9806057

EP 917684 A1 Based on WO 9806057

Designated States (Regional): AT BE CH DE ES FR GB IT LI NL

Language, Pages: FR 2752076 (19); WO 9806057 (F, 28); EP 917684 (F)

**Electric supply for mixed function microcircuit with or without contacts**

...G06K-007/00

...Abstract (Basic): USE - For **IC card** , IC mounted on portable support

...

...ADVANTAGE - Automatically manages distribution of two supply potentials  
inside microcircuit with **two** operating **modes** . Also avoids static  
electricity damage if user touches **contacts** .

Derwent Class: **T04** ;

International Patent Class (Main): **G06K-007/00** ...

...G06K-019/07

International Patent Class (Additional): **G06K-007/06** ...

...G06K-007/08

Manual Codes (EPI/S-X): **T04-K01** ...

**12/3,K/20 (Item 20 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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011558775 \*\*Image available\*\*

WPI Acc No: 97-535256/199749

XRPX Acc No: N97-445673

**Monitoring apparatus for events relating to timing inter-relationships -  
has target program running on at least one target processor on common bus**

Patent Assignee: BBN CORP (BBNB-N)

Inventor: HATHAWAY R E; ROEBER F J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
US 5682328	A	19971028	US 96710101	A	19960911	H04J-001/16	199749 B

Priority Applications (No Type Date): US 96710101 A 19960911

Language, Pages: US 5682328 (15)

...Abstract (Basic): The monitoring apparatus has a control **card**  
installed on the common bus, via a bus **interface** . The **card** has a  
control processor running a control program, and a time stamp clock,  
accessible to...

...processor and over the common bus, providing a time stamp for the  
events. A network **interface** is accessible to the processor to effect  
communication between the processor and the host computer...

...A control **card** memory has a first memory portion storing the control  
program to effect the functionality of the **card** , a second memory  
portion for storing event information received directly from the target  
processors. and...

...collection mechanism on the target processor collects event information  
for the target program. This has **two** operational **modes** , including a

first mode for storing event information in a second memory buffer, and  
a...  
Derwent Class: T01

12/3,K/21 (Item 21 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

011501844 \*\*Image available\*\*  
WPI Acc No: 97-479758/199744  
XRPX Acc No: N97-400214

**Reconfigurable computer network interface - reconfigures transceiver  
and reconfigurable controller by hardware set-up and operational software  
instructions to communicate in different network hardware protocols**

Patent Assignee: PREDACOMM INC (PRED-N)

Inventor: COLLINS M A

Number of Countries: 022 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
US 5671355	A	19970923	US 92905285	A	19920626	G06F-013/00	199744	B
			US 94265498	A	19940623			
			US 96713755	A	19960913			
WO 9814014	A2	19980402	WO 97US16375	A	19970912	H04Q-000/00	199820	
AU 9744180	A	19980417	AU 9744180	A	19970912	G06F-015/00	199834	

Priority Applications (No Type Date): US 96713755 A 19960913; US 92905285 A  
19920626; US 94265498 A 19940623

Filing Details:

Patent	Kind	Filing	Notes	Application	Patent
US 5671355	A	CIP of		US 92905285	
		CIP of		US 94265498	

WO 9814014 A2

Designated States (National): AU CA JP MX

Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LU MC

NL PT SE

AU 9744180 A Based on WO 9814014

Language, Pages: US 5671355 (12); WO 9814014 (E, 26)

**Reconfigurable computer network interface - ...**

**...transceiver and reconfigurable controller by hardware set-up and  
operational software instructions to communicate in different network  
hardware protocols**

...Abstract (Basic): The reconfigurable computer network **interface** (10)  
includes a reconfigurable controller (12), reconfigurable bus  
**interface** (22), and reconfigurable transceiver (14). The device (10)  
also includes a configuration controller (20) and...

...for storing configuration instructions. Configuration instructions are  
received from an external source. The reconfigurable bus **interface**  
(22) may be configured by hardware set-up and operational software  
instructions to emulate a bus **interface** for any of a number of  
different computer bus architectures...

...A bus adapter (26) connects between a bus port (39) associated with the  
reconfigurable bus **interface** (22) and the computer bus to provide the  
physical connection between the device (10) and...

...set-up and operational software instructions to communicate in any one  
of a number of **different** network hardware **protocols** . A media

connector (24a, 24b) cooperates with a transceiver port (23a, 23b) associated with the...

...hardware set-up and operational software instructions to communicate in any of a number of **different** software **protocols** .

...

...ADVANTAGE - Reconfigurable computer network **interface** operates as network **card** , bridge, router, brouter, or gateway between any type of computer and any type of computer

...Title Terms: **INTERFACE** ;

Derwent Class: **T01**

**12/3,K/22** (Item 22 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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011467720 \*\*Image available\*\*

WPI Acc No: 97-445627/199741

XRPX Acc No: N97-371248

**Electronic appts e.g. DC card used in PC - has mode switching circuit which is changed to two modes for controlling input-output operation of digital logic circuit and analog circuit**

Patent Assignee: FUJITSU LTD (FUJIT )

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
JP 9204507	A	19970805	JP 9613357	A	19960129	G06K-019/07	199741 B

Priority Applications (No Type Date): JP 9613357 A 19960129

Language, Pages: JP 9204507 (11)

**Electronic appts e.g. DC card used in PC...**

**...has mode switching circuit which is changed to two modes for controlling input-output operation of digital logic circuit and analog circuit**

...Abstract (Basic): logic unit (5-7) and an analog unit (8,9) which are connected with an **interface** circuit (3). Based on the input signal in an external connection terminal (2), a mode...

...During the first mode, the signal from the **interface** circuit is given to the digital unit where the signal is converted into the analog signal and is then processed. During the second mode, the signal from the **interface** circuit is directly given to the analog unit and is then processed...

...Title Terms: **CARD** ;

Derwent Class: **T01** ; ...

...**T02**

International Patent Class (Main): **G06K-019/07**

**12/3,K/23** (Item 23 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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011332482 \*\*Image available\*\*

WPI Acc No: 97-310386/199728

XRPX Acc No: N97-257155

**Electronic game with microprocessor control system, program and display -**

provides information to game players, with mechanism allowing players to input game codes to microprocessor, and with control system storing valid codes allowing access to enhanced game features, game using magnetic card reader to start

Patent Assignee: MIDWAY GAMES INC (MIDW-N)

Inventor: BOON E J; NICASTRO N D

Number of Countries: 070 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9719736	A1	19970605	WO 96US19743	A	19961119	A63F-009/24	199728 B
AU 9712869	A	19970619	AU 9712869	A	19961119	A63F-009/24	199741
EP 866728	A1	19980930	EP 96943703	A	19961119	A63F-009/24	199843
			WO 96US19743	A	19961119		

Priority Applications (No Type Date): US 95565976 A 19951201

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9719736	A1			

Designated States (National): AL AM AT AU AZ BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU IL IS JP KE KG KP KR KZ LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN

Designated States (Regional): AT BE CH DE DK EA ES FI FR GB GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG

AU 9712869	A	Based on	WO 9719736
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EP 866728	A1	Based on	WO 9719736
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Designated States (Regional): DE FR GB

Language, Pages: WO 9719736 (E, 15); EP 866728 (E)

... with control system storing valid codes allowing access to enhanced game features, game using magnetic card reader to start

...Abstract (Basic): match the valid codes. The mechanism for inputting the player inputs includes a magnetic stripe card reader (32) and the player operated switches (33). The valid codes are stored in a read...

...USE - For coin operated video games having more than one mode of play or class of use...

...Title Terms: CARD ;

...Derwent Class: T01 ; ...

...T05 ;

12/3,K/24 (Item 24 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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011281283 \*\*Image available\*\*

WPI Acc No: 97-259187/199723

XRPX Acc No: N97-214277

Chip card reader with coupling circuit performing all synchronisation - implements standard functions requiring timing constraints and bit manipulation with software management of all protocol-specific functions

Patent Assignee: GEMPLUS SCA (GEMP-N)

Inventor: PRADEN A; PRADEN A M

Number of Countries: 021 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9715895	A1	19970501	WO 96FR1634	A	19961018	G06K-007/06	199723 B
FR 2740240	A1	19970425	FR 9512403	A	19951020	G06F-013/42	199724
EP 800682	A1	19971015	EP 96934937	A	19961018	G06K-007/06	199746

Ginger Roberts - Search Report

WO 96FR1634 A 19961018  
JP 10511491 W 19981104 WO 96FR1634 A 19961018 G06K-017/00 199903  
JP 97516347 A 19961018

Priority Applications (No Type Date): FR 9512403 A 19951020

Filing Details:

Patent Kind Filing Notes Application Patent  
WO 9715895 A1  
Designated States (National): CA JP US  
Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LU MC  
NL PT SE

EP 800682 A1 Based on WO 9715895  
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LI LU MC  
NL PT SE

JP 10511491 W Based on WO 9715895  
Language, Pages: WO 9715895 (F, 6); EP 800682 (F); JP 10511491 (33)

Chip card reader with coupling circuit performing all  
synchronisation...  
...G06K-007/06

...Abstract (Basic): The **reader** is equipped with contact studs (3-7)  
compatible with those of the **card** (2), and a coupling circuit (13)  
for conveying signals between these studs and the microprocessor...

...USE/ADVANTAGE - For **cards** affording access to subscription services,  
reduces load on microprocessor while conserving flexibility for  
implementation of **different** interaction **protocols** .

...Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...T04  
...International Patent Class (Main): **G06K-007/06** ...

...G06K-017/00  
International Patent Class (Additional): **G06K-007/00** ...

...G06K-007/016 ...

...G06K-019/07  
...Manual Codes (EPI/S-X): **T04-K02**

12/3,K/25 (Item 25 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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011250348 \*\*Image available\*\*  
WPI Acc No: 97-228251/199721  
XRPX Acc No: N97-188660

**Medical diagnostic and or therapy system with memory unit - determines  
charges for number of functional modes that have different costing rates  
in memory**

Patent Assignee: MUELLER & SEBASTIANI ELEKTRONIK GMBH (MUEL-N)  
Number of Countries: 001 Number of Patents: 001  
Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
DE 29702277	U1	19970410	DE 97U2002277	U	19970210	G07C-003/10		199721 B

Priority Applications (No Type Date): DE 97U2002277 U 19970210  
Language, Pages: DE 29702277 (20)

...Abstract (Basic): value is logged [A1,B1, etc]. Charge rate factors [a,b,c,d] for the **different** function **modes** are logged in a coupled memory [2]. Values are fed to a processor [3]. Access is provided by data **card** [9] and a **reader** and also via an **interface** [7]. A connection is also made to a FAX modem [6] and printer...

...Derwent Class: **T01** ; ...

...**T05**

**12/3,K/26** (Item 26 from file: 351)  
 DIALOG(R)File 351:DERWENT WPI  
 (c) 2000 DERWENT INFO LTD. All rts. reserv.

011168504 \*\*Image available\*\*  
 WPI Acc No: 97-146429/199714  
 XREF Acc No: N97-121060

Smart card with multiple function elements - has range of functional elements embedded in card that allows different modes of data and energy transfers to be obtained

Patent Assignee: ANGEWANDTE DIGITAL ELEKTRONIK (ANGE-N)

Inventor: KREFT H

Number of Countries: 034 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
DE 19530823	A1	19970227	DE 1030823	A	19950823	G06K-019/07	199714 B
WO 9708645	A2	19970306	WO 96DE1569	A	19960823	G06K-000/00	199716
AU 9710918	A	19970319	AU 9710918	A	19960823	G06K-019/07	199728
WO 9708645	A3	19970417	WO 96DE1569	A	19960823	G06K-019/07	199731

Priority Applications (No Type Date): DE 1030823 A 19950823

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9708645	A2			

Designated States (National): AU BR CA CN CZ HU JP KR MX NO PL RU SG TR  
 UA US

Designated States (Regional): AT BE CH DE DK EA ES FI FR GB GR IE IT LU  
 MC NL PT SE

AU 9710918 A Based on WO 9708645

Language, Pages: DE 19530823 (6); WO 9708645 (G, 11)

Smart card with multiple function elements...

...has range of functional elements embedded in card that allows different modes of data and energy transfers to be obtained

...**G06K-000/00**

...Abstract (Basic): The **smart** card [1] has a coil [3], capacitors [4] **contacts** [5], IC chips [6], opto electronic receiver and transmitter elements [16,17], as well as...

...elements for data and energy transfer to and from an external item of equipment. The **card** also has a key pad [18] to be electronically or manually activated for entry of data into the **card**. The combination of elements built into the **card** allows a range of different data and energy exchange processes to be selected...

...Title Terms: **CARD** ;

Derwent Class: **T04** ;

International Patent Class (Main): **G06K-000/00** ...

...**G06K-019/07**

Manual Codes (EPI/S-X): **T04-K** ...

12/3,K/27 (Item 27 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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011027502 \*\*Image available\*\*

WPI Acc No: 97-005426/199701

XRPX Acc No: N97-004971

**Computer gateway for e.g. thin Ethernet local-area network, twisted wire ethernet LAN - uses standardization of protocols which enables computers with different protocols to communicate within network**

Patent Assignee: ACCESS YG (ACCE-N)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
JP 8274835	A	19961018	JP 9594236	A	19950329	H04L-029/06	199701 B

Priority Applications (No Type Date): JP 9594236 A 19950329

Language, Pages: JP 8274835 (8)

... **uses standardization of protocols which enables computers with different protocols to communicate within network**

...Abstract (Basic): USE/ADVANTAGE - For e.g. personal computer, word processor, printer, facsimile, modem, **card reader** . Enables electronic devices to communicate without use of special software and hardware; simplifies installation; uses...

Derwent Class: T01 ;

12/3,K/28 (Item 28 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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011023516 \*\*Image available\*\*

WPI Acc No: 97-001440/199701

XRPX Acc No: N97-001244

**IC card reader writer - identifies protocol type of host unit and allows data transfer between host unit and IC card subsequent to conversion if type is different**

Patent Assignee: TOSHIBA KK (TOKE )

Inventor: KONDOU Y

Number of Countries: 005 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 744708	A2	19961127	EP 96108100	A	19960521	G06K-017/00	199701 B
JP 8315090	A	19961129	JP 95123768	A	19950523	G06K-017/00	199707
EP 744708	A3	19970813	EP 96108100	A	19960521	G06K-017/00	199745
US 5799171	A	19980825	US 96651483	A	19960522	G06F-013/00	199841

Priority Applications (No Type Date): JP 95123768 A 19950523

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 744708	A2			

Designated States (Regional): DE FR GB

Language, Pages: EP 744708 (E, 9); JP 8315090 (8)

IC card reader **writer...**

...**identifies protocol type of host unit and allows data transfer between host unit and IC card subsequent to conversion if type is different** .  
...G06K-017/00



...Abstract (Basic): The **reader** / writer identifies a protocol type of the **IC card** based on a specific one of the data sent from the **card** when an inserted **IC card** is activated. When the protocol type of the **card** corresponds to the first protocol same type as the host units, data is transferred between the **IC card** and the host unit directly...

...second type different to the first, data is transferred between the host unit and the **IC card** after performing protocol conversion of transfer data...

...ADVANTAGE - **Reader** /write can handle **cards** with **different protocol** types...

...Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...**T04**  
...International Patent Class (Main): **G06K-017/00**  
Manual Codes (EPI/S-X): **T01-C07C1** ...

...**T04-K02**

**12/3,K/29** (Item 29 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

011021946 \*\*Image available\*\*  
WPI Acc No: 96-518896/199651  
XRPX Acc No: N96-437195

Card reader/writer which interfaces host application program with data storage card - translates high level language commands of host program to corresponding sequences of low level commands for reading and writing to data storage card

Patent Assignee: SMARTMOVE NZ LTD (SMAR-N)  
Inventor: ZUPPICICH A N  
Number of Countries: 071 Number of Patents: 005  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9636051	A1	19961114	WO 96NZ38	A	19960509	G11C-007/00	199651 B
AU 9655179	A	19961129	AU 9655179	A	19960509	G11C-007/00	199712
EP 826215	A1	19980304	EP 96912337	A	19960509	G11C-007/00	199813
			WO 96NZ38	A	19960509		
AU 687312	B	19980219	AU 9655179	A	19960509	G11C-007/00	199824
JP 11505049	W	19990511	JP 96533975	A	19960509	G06K-017/00	199929
			WO 96NZ38	A	19960509		

Priority Applications (No Type Date): NZ 272094 A 19950509

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9636051	A1			

Designated States (National): AL AM AT AU AZ BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU IS JP KE KG KP KR KZ LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TR TT UA UG US UZ VN  
Designated States (Regional): AT BE CH DE DK EA ES FI FR GB GR IE IT KE LS LU MC MW NL OA PT SD SE SZ UG

AU 9655179	A	Based on	WO 9636051
EP 826215	A1	Based on	WO 9636051
		Designated States (Regional): AT BE CH DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE	
AU 687312	B	Previous Publ. Based on	AU 9655179 WO 9636051

JP 11505049 W Based on WO 9636051  
 Language, Pages: WO 9636051 (E, 41); EP 826215 (E); JP 11505049 (45)

Card reader/writer which interfaces host application program with  
 data storage card - ...

...program to corresponding sequences of low level commands for reading and  
 writing to data storage card  
 ...G06K-017/00

...Abstract (Basic): The card reader /writer can respond to an  
 application program using at least one designated high level language.  
 Several low level protocol sets are stored which each correspond to  
 a known data storage card type. The card reader is able to  
 establish the card type for any card interfaced to it for which  
 it has a protocol set. The reader selects from its store of protocols  
 the appropriate low level protocol for the established card type...

...The card reader /writer reads and translates high level language  
 commands from the host program to corresponding commands within the  
 established low level protocol and writes these low level commands to  
 the card. The reader /writer reads commands in the established low  
 level protocol from the card and translates them to a corresponding  
 command in the high level language. These commands are...

...ADVANTAGE - Card reader can read a variety of magnetic card types  
 as well as a variety of types of chip cards.

Title Terms: CARD ;  
 Derwent Class: T04 ;  
 International Patent Class (Main): G06K-017/00 ...  
 International Patent Class (Additional): G06K-017/00  
 ...Manual Codes (EPI/S-X): T04-K02

12/3,K/30 (Item 30 from file: 351)  
 DIALOG(R) File 351:DERWENT WPI  
 (c) 2000 DERWENT INFO LTD. All rts. reserv.

010876128 \*\*Image available\*\*  
 WPI Acc No: 96-373079/199638  
 XRPX Acc No: N96-313903

Reading and writing from and to IC cards with different protocol  
 rules - receives command from host device with protocol information  
 corresp. to one protocol which is analysed, sets protocol for  
 communication with card based on protocol data

Patent Assignee: TOSHIBA CORP (TOKE ); TOSHIBA KK (TOKE )  
 Inventor: TOSHIYUKI K; YOUKO K; KAWAGISHI T; KONDOU Y  
 Number of Countries: 007 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 727759	A1	19960821	EP 96102094	A	19960213	G06K-019/07	199638 B
JP 8227444	A	19960903	JP 9530712	A	19950220	G06K-017/00	199645
CN 1136684	A	19961127	CN 96103544	A	19960216	G06K-007/00	199805
US 5798507	A	19980825	US 96603207	A	19960220	G06K-005/00	199841

Priority Applications (No Type Date): JP 9530712 A 19950220

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 727759	A1			

Designated States (Regional): DE FR GB SE  
 Language, Pages: EP 727759 (E, 17); JP 8227444 (11)

Reading and writing from and to IC cards with different protocol rules...

...with protocol information corresp. to one protocol which is analysed, sets protocol for communication with card based on protocol data  
...G06K-017/00

...Abstract (Basic): The IC card reader and writer (1) includes a receiver (21) for receiving a command from a host device...

...A protocol is set for communication with an IC card in accordance with the protocol information contained in the analysed command. A communication mechanism communicates with the IC card based on the protocol set by the setting mechanism...

...ADVANTAGE - Can cope with IC cards of different types of protocols, copes with function inherent to each protocol and significantly increases its convenience...

...Title Terms: CARD ;  
Derwent Class: T01 ; ...

...T04

International Patent Class (Main): G06K-005/00 ...

...G06K-007/00 ...

...G06K-017/00 ...

...G06K-019/07

International Patent Class (Additional): G06K-007/08

Manual Codes (EPI/S-X): T01-C07C1 ...

...T04-K02

12/3,K/31 (Item 31 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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010792141 \*\*Image available\*\*

WPI Acc No: 96-289094/199630

XRFX Acc No: N96-242609

Magnetic disk drive interface with PLU - stores and retrieves information, operation of disk drive and PLU are controlled, PLU is configured as interface enabling communication by disk drive over bus, PCMCIA card is coupled to application over bus

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC ); IBM CORP (IBMC )

Inventor: SHAFE M K; SHAFE' M K

Number of Countries: 005 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 718751	A2	19960626	EP 95309160	A	19951215	G06F-003/06	199630 B
JP 8249127	A	19960927	JP 95330388	A	19951219	G06F-003/06	199649
EP 718751	A3	19970212	EP 95309160	A	19951215	G06F-003/06	199715
US 5918068	A	19990629	US 94363464	A	19941223	G06F-013/00	199932
			US 97882018	A	19970603		

Priority Applications (No Type Date): US 94363464 A 19941223; US 97882018 A 19970603

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 718751	A2			

Designated States (Regional): DE FR GB  
US 5918068 A Cont of US 94363464  
Language, Pages: EP 718751 (E, 17); JP 8249127 (13)

**Magnetic disk drive interface with PLU...**

**...and retrieves information, operation of disk drive and PLU are controlled, PLU is configured as interface enabling communication by disk drive over bus, PCMCIA card is coupled to application over bus**

...Abstract (Basic): disk drive and the PLU to control their operation. The PLU is configured as an **interface** to enable communication by the disk drive over a bus...

...A **card** enclosure houses the appts.. The **card** is electrically coupled to an application (25) which communicates over the bus. The enclosure includes a PCMCIA type **card** . The disk drive is controlled by a hard disk controller (HDC) (23...

...USE - Relates to magnetic disk drive **interfaces** with component sized disk drive...

...ADVANTAGE - Provides component level disk drive that can be adapted to **different** types of communication **protocols** .

...Title Terms: **INTERFACE** ;  
Derwent Class: **T01** ; ...

...T03

12/3,K/32 (Item 32 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

010743943 \*\*Image available\*\*  
WPI Acc No: 96-240898/199625  
XRPX Acc No: N96-201633

**Computer resource regulation password protection for adult files against use by others - has system for operating computer in any of two different interactive modes, switch is included with two states, each of which when enabled causes one mode to be active, access control device allows user to change state of switch**

Patent Assignee: COMPAQ COMPUTER CORP (COPQ )

Inventor: ROBINSON T L

Number of Countries: 006 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
EP 713168	A1	19960522	EP 95308068	A	19951110	G06F-001/00		199625 B
CA 2162644	A	19960516	CA 2162644	A	19951110	G06F-012/14		199637
JP 8249154	A	19960927	JP 95321002	A	19951115	G06F-003/14		199649

Priority Applications (No Type Date): US 94339840 A I9941115

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 713168	A1			

Designated States (Regional): DE FR GB NL  
Language, Pages: EP 713168 (E, 12); JP 8249154 (9)

**... has system for operating computer in any of two different interactive modes, switch is included with two states, each of which when enabled causes one mode to...**

...Abstract (Basic): computer resource regulation appts. includes a system for operating the computer (12) in any of **two different** interactive **modes** . A switch is included with two states. Each state when enabled causes one of the...

...21), or a dedicated input device for entering a security code, or a magnetic strip **reader** . Alternatively, the control device is a token **reader** and a set of tokens read by the **reader** , each token associated with one of the modes, or a PCMCIA **card** input device with a **card** for each mode

Derwent Class: **T01**

**12/3,K/33 (Item 33 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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010545385 \*\*Image available\*\*

WPI Acc No: 96-042338/199605

XRPX Acc No: N96-035503

**Mobile phone communications system for one-to-one and one-to-many computer data transmission - uses add-on electronic integrated circuit board with transmitter-receiver antenna interface, which uses concept of open numbers for transmission not requiring response from recipient, and connected to computer via interface**

Patent Assignee: MALIK G S J (MALI-I)

Inventor: MALIK G S J

Number of Countries: 065 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
GB 2290923	A	19960110	GB 953753	A	19950224	H04Q-007/32		199605 B
WO 9626618	A1	19960829	WO 95GB2100	A	19950908	H04Q-007/32		199640
AU 9533960	A	19960911	AU 9533960	A	19950908	H04Q-007/32		199651

Priority Applications (No Type Date): GB 9413384 A 19940702

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9626618	A1			

Designated States (National): AM AT AU BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU IS JP KE KG KP KR KZ LK LR LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TT UA UG US UZ VN

Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT KE LU MC MW NL OA PT SD SE SZ UG

AU 9533960 A Based on WO 9626618

Language, Pages: GB 2290923 (9); WO 9626618 (E, 10)

**... uses add-on electronic integrated circuit board with transmitter-receiver antenna interface, which uses concept of open numbers for transmission not requiring response from recipient, and connected to computer via interface**

...Abstract (Basic): The mobile phone communications system is facilitated by an add-on **card** in the form of a circuit board which has a CPU connected to a singular/plural antenna **interface** for transmission/reception (1), EPROM (2), ROM (3) RAM (6) and CMOS chips, a battery (4), a clock (5), DIP switches (8) and an **interface** to the host computer bay and power pins (9...

...The **card** allows for connection to a mobile phone network of the users choice, and has an...

...one, one-to-many or many-to-many arrangement, in digital or analogue

mode, or **different modes** for different numbers  
 ...Title Terms: **INTERFACE** ;  
 Derwent Class: **T01** ;

**12/3,K/34 (Item 34 from file: 351)**  
 DIALOG(R)File 351:DERWENT WPI  
 (c) 2000 DERWENT INFO LTD. All rts. reserv.

010543478 \*\*Image available\*\*  
 WPI Acc No: 96-040432/199604  
 XRPX Acc No: N96-033961

**Dual function interface for PCMCIA compatible interface cards - sets unique combination of address and-or control lines as defined by alternative interface system specification, and automatically and transparently switches to latter**

Patent Assignee: MOTOROLA INC (MOTI )  
 Inventor: BEAUDOIN D; MOSS B; RETZER M H  
 Number of Countries: 064 Number of Patents: 003  
 Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9534086	A1	19951214	WO 95US4211	A	19950405	H01J-013/00	199604 B
AU 9522790	A	19960104	AU 9522790	A	19950405	H01J-013/00	199613
US 5793989	A	19980811	US 94253995	A	19940603	G06F-013/00	199839
			US 96746048	A	19961106		

Priority Applications (No Type Date): US 94253995 A 19940603; US 96746048 A 19961106

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9534086	A1			
Designated States (National): AM AT AU BB BG BR BY CA CH CN CZ DE DK EE ES FI GB GE HU IS JP KE KG KP KR KZ LK LR LT LU LV MD MG MN MW MX NO NZ PL PT RO RU SD SE SG SI SK TJ TM TT UA UG UZ VN				
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT KE LU MC MW NL OA PT SD SE SZ UG				
AU 9522790	A	Based on	WO 9534086	
US 5793989	A	Cont of	US 94253995	
Language, Pages: WO 9534086 (E, 17)				

**Dual function interface for PCMCIA compatible interface cards - ... sets unique combination of address and-or control lines as defined by alternative interface system specification, and automatically and transparently switches to latter**

...Abstract (Basic): The appts. for multiplexing an **interface** between a computer and a peripheral device from a Personal Computer Memory **Card** International Association (PCMCIA) type mode of operation to...

...of address and control signals. A device responds to the code and switches between the **two** operational **modes** .

...Title Terms: **INTERFACE** ;  
 ...Derwent Class: **T01** ;

**12/3,K/35 (Item 35 from file: 351)**  
 DIALOG(R)File 351:DERWENT WPI  
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010519456

WPI Acc No: 96-016407/199602

XRAM Acc No: C96-005238

XRPX Acc No: N96-014195

**Reversible thermal sensitive sheet useful for electronic blackboard -  
mfd. by laminating reversible thermal sensitive layers on a translucent  
sheet contg conductive wires, for adjusted light permeability**

Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU )

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
JP 7290841	A	19951107	JP 9520840	A	19950208	B41M-005/36	199602 B

Priority Applications (No Type Date): JP 9433294 A 19940303

Language, Pages: JP 7290841 (10)

...Abstract (Basic): b) reversible thermal sensitive layers reversibly  
changing their transparency or hue by **two** heating **modes** , a  
high-temp mode and a low-temp mode and formed on the translucent sheet

...

...USE - The reversible thermal sensitive sheet and its application system  
are used for a **reader** /writer for information memory **card** , facsimile  
machine, word processor, printer, or electronic blackboard...

...Derwent Class: **T04** ;

**12/3,K/36 (Item 36 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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010383742 \*\*Image available\*\*

WPI Acc No: 95-285056/199538

XRPX Acc No: N95-217060

**Card payment telephone for table or wall mounting - has base which can  
be rotated w.r.t. upper housing to convert from table to wall use**

Patent Assignee: SIEMENS AG (SIEI )

Inventor: GRASSL E; LUDWIG M

Number of Countries: 014 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 668684	A1	19950823	EP 95102018	A	19950214	H04M-001/02	199538 B

Priority Applications (No Type Date): DE 94U2718 U 19940218

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 668684	A1			

Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LI NL PT SE

Language, Pages: EP 668684 (G, 5)

**Card payment telephone for table or wall mounting...**

...Abstract (Basic): a base (3) which is connected to the upper housing  
part (2) and includes a **card reader** .

...

...used either as table-top or wall mounted telephone and is easy to  
convert between **two modes** .

Title Terms: **CARD** ;

Derwent Class: **T05** ;

**12/3,K/37 (Item 37 from file: 351)**

DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

009998790 \*\*Image available\*\*

WPI Acc No: 94-266501/199433

XRPX Acc No: N94-209740

**Communication procedure for portable information carriers e.g. smart memory card, for portable microcomputer - using communication layer between application programs and card inserts without using single communication protocol**

Patent Assignee: GEMPLUS CARD INT SA (GEMP-N)

Inventor: LEROUX J; LE ROUX J

Number of Countries: 019 Number of Patents: 009

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
FR 2701133	A1	19940805	FR 931235	A	19930204	G06F-013/00	199433 B
WO 9418628	A1	19940818	WO 94FR127	A	19940203	G06F-013/38	199434
EP 682792	A1	19951122	EP 94906238	A	19940203	G06F-013/38	199551
			WO 94FR127	A	19940203		
JP 8502846	W	19960326	JP 94517716	A	19940203	G06K-017/00	199644
			WO 94FR127	A	19940203		
US 5651116	A	19970722	WO 94FR127	A	19940203	H01J-013/00	199735
			US 95501071	A	19950918		
SG 48123	A1	19980417	SG 967159	A	19940203	H01J-013/00	199827
EP 682792	B1	19980722	EP 94906238	A	19940203	G06F-013/38	199833
			WO 94FR127	A	19940203		
DE 69411889	E	19980827	DE 611889	A	19940203	G06F-013/38	199840
			EP 94906238	A	19940203		
			WO 94FR127	A	19940203		
ES 2118382	T3	19980916	EP 94906238	A	19940203	G06F-013/38	199848

Priority Applications (No Type Date): FR 931235 A 19930204

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
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WO 9418628	A1			
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Designated States (National): JP US

Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL

PT SE

EP 682792	A1	Based on	WO 9418628
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Designated States (Regional): DE ES GB IT NL

JP 8502846	W	Based on	WO 9418628
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US 5651116	A	Based on	WO 9418628
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EP 682792	B1	Based on	WO 9418628
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Designated States (Regional): DE ES GB IT NL

DE 69411889	E	Based on	EP 682792
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Based on

WO 9418628

ES 2118382	T3	Based on	EP 682792
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EP 682792

Language, Pages: FR 2701133 (19); WO 9418628 (F, 22); EP 682792 (F, 19); JP 8502846 (21); US 5651116 (9); EP 682792 (F)

**Communication procedure for portable information carriers e.g. smart memory card, for portable microcomputer...**

**...using communication layer between application programs and card inserts without using single communication protocol**

**...G06K-017/00**

...Abstract (Basic): an application programme and portable information aids (10, 20) of various types has a communication **card** insert between the application program and the aids and does not use a single protocol...

...The communication procedure for reception of an application program



extracts the address message using the **reader** , detects the presence of an aid at this address and identifies its type...

...Depending upon the type of **card** inserted, such as a **chip card** with surface **contacts** an intelligent memory **card** with an end connector, messages are transmitted direct to the support. The message is analysed and coded to make it compatible with the support. The **reader** has two types of connectors to receive the different types of **cards** and make the appropriate electrical contact. A support (33) holds the **card** and after **card** insertion pushes the **card** against the **contacts** (32...

...USE - Insertion of electronic **cards** of different types into same insert and discriminating between communication protocols...

...Abstract (Equivalent): A method of communicating between a microcomputer and different **integrated circuit cards** , the microcomputer being associated with an application program which uses functions borne by the different **integrated circuit cards** , the different **integrated circuit cards** being inserted in a **reader** connected to the microcomputer, and the different **integrated circuit cards** utilizing **different** communication **protocols** , the method comprising the steps of...

...adding a communication layer between the application program and the **integrated circuit cards** ; and...

...using only a single communication protocol to access the different **integrated circuit cards** which utilize **different** communication **protocols** , the using step being performed by the application program and being made possible by the...

...Title Terms: **CARD** ;

Derwent Class: **T01** ; ...

...**T04** ;

...International Patent Class (Main): **G06K-017/00**

...International Patent Class (Additional): **G06K-007/00** ...

...**G06K-007/06** ...

...**G06K-019/07**

...Manual Codes (EPI/S-X): **T04-K02**

**12/3,K/38** (Item 38 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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009959559 \*\*Image available\*\*

WPI Acc No: 94-227272/199428

XRPX Acc No: N94-179163

**Portable personal computer modem - has removable multi-country modem card slotting into computer with interface box and modem connection to telephone line**

Patent Assignee: PNB SA (PNBP-N)

Inventor: BOCQUET N; PESQUIE-NIKITINE I

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
FR 2699772	A1	19940624	FR 9215619	A	19921223	H04L-029/12		199428 B

Priority Applications (No Type Date): FR 9215619 A 19921223

Language, Pages: FR 2699772 (10)

... has removable multi-country modem card slotting into computer with interface box and modem connection to telephone line

...Abstract (Basic): The portable personal computer (2) has a removable modem **card** (3) which is insertable into a slot in the computer face. A modem connection (4) connects to the **card** and an **interface** box (5). The **interface** plugs into the outer wall of the computer...

...The **interface** box is connected via a connection plug (13a) to a multiple wire cable (7) and...

...Title Terms: **CARD** ;  
Derwent Class: **T01** ;

**12/3,K/39** (Item 39 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

009693211 \*\*Image available\*\*  
WPI Acc No: 93-386765/199348  
XRPX Acc No: N93-298635

**ISDN based high speed communication system - mounted on PC card, processes data encoded under various protocols and performs data compression, encryption and facsimile applications.**

Patent Assignee: CONNECTIVE STRATEGIES INC (CONN-N); BROWN C D (BROW-I); CROSS W C (CROS-I); DOVE R E (DOVE-I); HELLER P W T (HELL-I); HERGERT J K (HERG-I); LARSEN A J (LARS-I)  
Inventor: BROWN C D; CROSS W C; DOVE R E; HELLER P W T; HERGERT J K; LARSEN A J

Number of Countries: 022 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
WO 9323809	A1	19931125	WO 93US4470	A	19930517	G06F-013/00	199348 B
AU 9342454	A	19931213	AU 9342454	A	19930517	G06F-013/00	199413
US 5832240	A	19981103	US 92883862	A	19920515	G06F-013/00	199851
			US 94225877	A	19940411		
			US 96585607	A	19960111		
			US 97843114	A	19970428		

Priority Applications (No Type Date): US 92883862 A 19920515; US 94225877 A 19940411; US 96585607 A 19960111; US 97843114 A 19970428

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
WO 9323809	A1			
Designated States (National): AU JP KR RU UA				
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE				

AU 9342454	A	Based on	WO 9323809
US 5832240	A	Div ex	US 92883862
		Cont of	US 94225877
		Cont of	US 96585607

Language, Pages: WO 9323809 (E, 85)

... **mounted on PC card, processes data encoded under various protocols and performs data compression, encryption and facsimile applications.**

...Abstract (Basic): The computer **interface** circuit (32) couples a terminal computer to shared memory (40). A protocol processor (44) is coupled to the shared memory and to a serial communications processor (48). An ISDN **interface** (88) transfers data from the ISDN network to the serial communications circuit to the shared...

...Title Terms: **CARD** ;

Derwent Class: **T01** ;**12/3,K/40 (Item 40 from file: 351)**

DIALOG(R) File 351:DERWENT WPI

(c) 2000 DERWENT INFO LTD. All rts. reserv.

009551558 \*\*Image available\*\*

WPI Acc No: 93-245105/199331

Smart card **having** multiple **communication** protocol - **uses** protocol  
**detector to select and activate suitable protocol conversion circuit to**  
**allow card to operate**

Patent Assignee: GEMPLUS CARD INT SA (GEMP-N)

Inventor: KOWALSKI J

Number of Countries: 008 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 554164	A1	19930804	EP 93400187	A	19930126	G06K-007/00	199331 B
FR 2686998	A1	19930806	FR 921001	A	19920130	G06K-019/073	199344
JP 6020119	A	19940128	JP 9336123	A	19930201	G06K-019/07	199409
US 5420412	A	19950530	US 938517	A	19930125	G06K-019/06	199527
EP 554164	B1	19970305	EP 93400187	A	19930126	G06K-007/00	199714
DE 69308336	E	19970410	DE 608336	A	19930126	G06K-007/00	199720
			EP 93400187	A	19930126		
ES 2098688	T3	19970501	EP 93400187	A	19930126	G06K-007/00	199724
SG 48082	A1	19980417	SG 966916	A	19930126	G06K-000/00	199827

Priority Applications (No Type Date): FR 921001 A 19920130

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 554164	A1			

Designated States (Regional): DE ES GB IT

EP 554164 B1

Designated States (Regional): DE ES GB IT

DE 69308336 E Based on EP 554164

ES 2098688 T3 Based on EP 554164

Language, Pages: EP 554164 (F); US 5420412 (9); EP 554164 (F, 11)

Smart card **having** multiple **communication** protocol - ...

...**uses** protocol **detector to select and activate suitable protocol**  
**conversion circuit to allow card to operate**  
 ...G06K-019/073

...Abstract (Basic): The **card** has credit storage and alteration  
 processing, and has in addition **multiple protocol** conversion  
 circuits (CNV1,CNV2,CNV3), each compatible with a **different protocol**  
 , that accept signals under **different protocols** and convert them to  
 signals that are executable by the **card** .

...ADVANTAGE - Allows single pre-paid telephone **card** to be used with  
 telephone systems from different suppliers

...Abstract (Equivalent): **Chip card** capable of communicating with a  
**card reader** using **several different** communication **protocols** ,  
 characterised in that it has...

...each conversion circuit being capable of converting into instructions  
 that can be executed by the **card** electric signals received from the  
**reader** in compliance with a given protocol, each of the different  
 conversion circuits corresponding to a **different** communication  
**protocol** ,

- ...

...the conversion circuit being capable of generating specific instructions that can be executed by the **card** , these specific instructions being used to select one of the conversion circuits and being generated from the electric signals received by the **reader** which can be generated in all the protocols

...Abstract (Equivalent): In order to permit communications between **readers** operating according to **several different** communication **protocols** , the **card** comprises several conversion circuits (CNV1, CNV2, CNV3), each of which is able to convert into instructions performable by the **card** the electrical signals received from the **reader** according to a given protocol, each of the different conversion circuits corresponding to a **different** communication **protocol** ,

...

...The latter can produce specific instructions performable by the **card** , the specific instructions being used for the selection of one of the conversion circuits and...

...ADVANTAGE - Able to insert **cards** in **readers** operating according to **different** communication **protocols** .

...Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...**T04** ;  
International Patent Class (Main): **G06K-000/00** ...

...**G06K-007/00** ...

...**G06K-019/06** ...  
...**G06K-019/07** ...

...**G06K-019/073**  
...Manual Codes (EPI/S-X): **T04-K01**

**12/3,K/41** (Item 41 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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009129507

WPI Acc No: 92-256941/199231

**Contour tracking system using eight photo-sensors - has multi- mode standard optical fibre digitiser, zero reference photo-sensor and interface card NoAbstract**

Patent Assignee: KOREA ADV INST SCI & TECHN (KOAD )

Inventor: NAH S; PARK S

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
KR 9008517	B	19901124	KR 8713112	A	19871120	G05B-019/403	199231 B

Priority Applications (No Type Date): KR 8713112 A 19871120

... **has multi- mode standard optical fibre digitiser, zero reference photo-sensor and interface card NoAbstract**

...Title Terms: **INTERFACE** ;  
Derwent Class: **T06** ;

**12/3,K/42** (Item 42 from file: 351)  
DIALOG(R)File 351:DERWENT WPI

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009015978

WPI Acc No: 92-143316/199218

Multiple **payment** mode **internal modular public telephone - consists of analogue, logic and card reader units with coin store and selector**  
**NoAbstract**

Patent Assignee: TELEFON DE ESPAN (TELE-N)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
ES 2026109	A	19920401	ES 91807	A	19910326		199218 B

Priority Applications (No Type Date): ES 91807 A 19910326

Multiple **payment** mode **internal modular public telephone...**

**...consists of analogue, logic and card reader units with coin store and selector** **NoAbstract**

...Title Terms: **CARD** ;

Derwent Class: **T01** ; ...

...**T05** ;

International Patent Class (Additional): **G06K-007/00** ...

**12/3,K/43 (Item 43 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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008271377 \*\*Image available\*\*

WPI Acc No: 90-158378/199021

XRPX Acc No: N90-122934

**Communication between computer and wide area network - using coupler linked to internal data bus with sub-frames transmitted with sync. signal pulse intervals**

Patent Assignee: COLIN M (COLI-I); MOURO A A (MOUR-I); BULL SA (SELA )

Inventor: COLIN M; MOURO A A; MOURO A

Number of Countries: 016 Number of Patents: 010

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
FR 2637440	A	19900406	FR 8810989	A	19880818		199021 B
EP 423410	A	19910424	EP 89402874	A	19891018		199117 N
JP 3147163	A	19910624					199131 N
CA 2001068	A	19910419	CA 2001068	A	19891019	H04J-003/14	199232 N
US 5289465	A	19940222	US 89452512	A	19891219	H04J-003/16	199408 N
			US 92927800	A	19920810		
US 5371740	A	19941206	US 89452512	A	19891219	H04J-003/02	199503 N
			US 92927800	A	19920810		
			US 93140307	A	19931022		
CA 2001068	C	19950117	CA 2001068	A	19891019	H04J-003/14	199510 N
EP 423410	B1	19950628	EP 89402874	A	19891018	H04Q-011/04	199530 N
DE 68923282	E	19950803	DE 623282	A	19891018	H04Q-011/04	199536 N
			EP 89402874	A	19891018		
ES 2076225	T3	19951101	EP 89402874	A	19891018	H04Q-011/04	199550 N

Priority Applications (No Type Date): FR 8810989 A 19880818; EP 89402874 A 19891018; CA 2001068 A 19891019; US 92927800 A 19920810; US 93140307 A 19931022; DE 623282 A 19891018

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 423410	A			

Ginger Roberts - Search Report

Designated States (Regional): AT BE CH DE ES FR GB IT LI LU NL SE  
 US 5289465 A Cont of US 89452512  
 US 5371740 A Cont of US 89452512  
 Cont of US 92927800  
 Cont of US 5289465  
 EP 423410 B1  
 Designated States (Regional): AT BE CH DE ES FR GB GR IT LI LU NL SE  
 DE 68923282 E Based on EP 423410  
 ES 2076225 T3 Based on EP 423410  
 Language, Pages: US 5289465 (33); US 5371740 (34); CA 2001068 (F); EP  
 423410 (F, 40)  
 ...Abstract (Basic): The computer work station includes an **interface**  
 coupler (10) for communication via a telephone line, connected to an  
 internal bus (6). It is connected via this bus to a **multi -protocol**  
 communication controller (13) and a voice processor (15...  
 ...frames are inserted with variable lengths, allowing the connection of a  
 maximum of eight coupling **cards** . In four bits in the first two octets  
 of each sub-frame, the address of...  
 ...Abstract (Equivalent): A work station comprising a telephone  
 communication line **interface** coupler (10) characterised in that this  
 coupler is connected to an internal bus device (6...  
 ...Abstract (Equivalent): method of data transmission over an internal bus  
 of a workstation, effecting communication of an **interface** coupler and  
 a telephone line and devices adapted for various communication  
 functions...  
 ...a workstation for providing communication in cooperation with the  
 workstation between a telephone communication line **interface** coupler  
 (10) and a number of communication processors (13-16), uses a  
 synchronisation signal of...  
 Derwent Class: T01 ;

12/3,K/44 (Item 44 from file: 351)

DIALOG(R)File 351:DERWENT WPI  
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008114269 \*\*Image available\*\*  
 WPI Acc No: 90-001270/199001  
 XRPX Acc No: N90-000921

IC card read-write device for executing business transactions -  
 produces clock pulses frequency of which is variable in matching relation  
 to frequency of operating clock pulses

Patent Assignee: OKI ELECTRIC IND CO LTD (OKID )

Inventor: HIRATA H; TAKIZAWA T

Number of Countries: 006 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 347894	A	19891227	EP 89111333	A	19890622		199001 B
AU 8936582	A	19900503					199024
US 5070233	A	19911203	US 89367728	A	19890619		199151
CA 1319195	C	19930615	CA 603605	A	19890622	G06K-007/00	199329
EP 347894	B1	19950503	EP 89111333	A	19890622	G06K-007/00	199522
DE 68922439	E	19950608	DE 622439	A	19890622	G06K-007/00	199528
			EP 89111333	A	19890622		

Priority Applications (No Type Date): JP 88154620 A 19880624

Filing Details:

Patent Kind Filing Notes Application Patent

EP 347894 A

Designated States (Regional): DE FR GB

EP 347894 B1

Designated States (Regional): DE FR GB

DE 68922439 E Based on

EP 347894

Language, Pages: EP 347894 (E, 13); EP 347894 (E, 14)

IC card read-write device for executing business transactions...

...G06K-007/00

...Abstract (Basic): A microprocessor (22) in the IC card read-write device is programmed for determining whether or not an IC card (23) mounted in the device has an identifiable frequency mode...

...If the card has such a mode, the microprocessor reads the frequency data stored in the card to determine the operating clock frequency particular to the card. The read-write device then produces clock pulses of the frequency appropriate to the card for timing business transactions...

...ADVANTAGE - Read-write device is able to effect transactions with card having different frequency modes and different operating clock frequencies...

...Abstract (Equivalent): Method for operating an IC card reader/writer apparatus (5), including a first reference clock generating means (32) for generating first reference clock pulses having a predetermined first frequency to feed first clock pulses to an IC card (23) for interchanging data with the IC card (23) when said IC card is connected to said apparatus, comprising the steps of: supplying (S1) a first reset signal together with said first clock pulses to the IC card, receiving (S2) the answer-to-reset (ATR) signal, hereinafter referred to as the ATR signal, from the IC card (23) in response to the first reset signal, determining (S3) whether or not the ATR...

...data of the ATR signal and selecting (S5-S7) the operation frequency by which the reader/writer apparatus (5) communicates with the IC card (23), characterised by the steps of: providing a second reference clock generating means (21) for...

...reference clock pulses having a predetermined second frequency to feed second clock pulses to the IC card (23), if the ATR signal has no identifiable frequency mode, initializing (S9) the second reference...

...21), supplying (S10) a second reset signal together with said second clock pulses to the IC card (23), receiving (S11) the ATR signal from the IC card (23) in response to the second reset signal, determining (S12) whether or not the ATR...

...data of the ATR signal and selecting (S14-S16) the operation frequency by which the reader/writer apparatus (5) communicates with the IC card (23), if the ATR signal has no identifiable frequency mode, starting an error card processing...

...Abstract (Equivalent): The IC card reader/writer has frequency mode identifying means for determining whether or not an IC card loaded on it has an identifiable frequency. When the frequency mode of the IC card is identifiable, a clock frequency determiner reads frequency data representative of an operating clock frequency out of the IC card to determine an operating clock frequency particular to the card. Based on the determined operating clock frequency of the IC card, frequency setting means converts reference clock pulses being generated by reference clock generating means into clock pulses which match the clock frequency of the IC card, and feeds those clock pulses to the card. The reader/writer is capable of producing

clock pulses the frequency of which is variable in matching relation to the frequency of operating clock pulses of an IC card . USE - For business transactions processing appts...

...Title Terms: **CARD** ;  
Derwent Class: **T01** ; ...

...**T04**

International Patent Class (Main): **G06K-007/00**

...International Patent Class (Additional): **G06K-005/00** ...

...**G06K-019/07**

...Manual Codes (EPI/S-X): **T04-K**

**12/3,K/45 (Item 45 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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007438318

WPI Acc No: 88-072253/198811

XRPX Acc No: N88-054783

**Microcomputer interface circuit card module - has protocol converter for interconnecting devices with different communication protocols**

Patent Assignee: XMIT AG (XMIT-N)

Inventor: LEIBU J

Number of Countries: 013 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
EP 259786	A	19880316	EP 87112874	A	19870903			198811 B
DK 8704748	A	19880313						198824

Priority Applications (No Type Date): CH 863679 A 19860912

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 259786	A			

Designated States (Regional): AT BE CH DE FR GB GR IT LI LU NL SE

Language, Pages: EP 259786 (G, 8)

**Microcomputer interface circuit card module...**

**...has protocol converter for interconnecting devices with different communication protocols**

...Abstract (Basic): The module for emulation of a given **interface** has an **interface** circuit with a data bus on each side of the **interface** and a CPU for controlling a protocol converter. The first bus is coupled to the...

...Title Terms: **INTERFACE** ;

Derwent Class: **T01**

**12/3,K/46 (Item 46 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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007239544

WPI Acc No: 87-236552/198734

XRPX Acc No: N87-176945

**Programmable p-c compatible communication card - has controllers operable for converting data stream into predetermined format and configuration logic circuit**

Patent Assignee: INT BUSINESS MACHINES CORP (IBMC ); IBM CORP (IBMC )

Inventor: KAZAN H; KOHAKE R W; KOHAKE R



# Ginger Roberts - Search Report

Number of Countries: 005 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 233373	A	19870826	EP 86118000	A	19861223		198734 B
JP 62160849	A	19870716	JP 86275512	A	19861120		198734
US 4807282	A	19890221	US 85814436	A	19851230		198910
EP 233373	B1	19930901	EP 86118000	A	19861223	G06F-013/12	199335
DE 3688972	G	19931007	DE 3688972	A	19861223	G06F-013/12	199341
			EP 86118000	A	19861223		

Priority Applications (No Type Date): US 85814436 A 19851230

Filing Details:

Patent Kind Filing Notes Application Patent

EP 233373 A  
Designated States (Regional): DE FR GB

EP 233373 B1  
Designated States (Regional): DE FR GB

DE 3688972 G Based on EP 233373

Language, Pages: EP 233373 (E, 13); US 4807282 (12); EP 233373 (E, 15)

## Programmable p-c compatible communication card -

- ...Abstract (Basic): The **multi -protocol** communications adapter comprises a configuration control logic circuit (18) coupled to the processor the configuration...
- ...Abstract (Equivalent): A **multi -protocol** communication adapter device for interfacing a processor with a modem for providing communication between said...
- ...signals whenever a predetermined address range is being decoded on the address bus; and a **plurality** of **protocol** conversion controllers (20, 22, 24), including an SDLC controller (20), a bisynchronous controller (22) and...
- ...Abstract (Equivalent): The **multi -protocol** communications adapter (MPCA) is used to interconnect a Local Area Network (such as a store...
- ...A programmable configuration register is provided for selecting one of the protocol controllers. A controlled **interface** is provided for gating the selected controller onto the communications highway...
- ...The MPCA is packaged as a **card** or module. It is coupled to a primary computer that controls the local Area Network...
- ...Title Terms: **CARD** ;
- Derwent Class: **T01**

12/3,K/47 (Item 47 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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004622087

WPI Acc No: 86-125430/198620

XRPX Acc No: N86-092695

**Prepayment system for electricity meter - uses purchased card whose value is read into meter and then obliterated to prevent re-use**

Patent Assignee: LGZ LANDIS & GYR ZUG AG (LANI )

Inventor: EBERLI A; ERISMANN R; WULLSCHLES P

Number of Countries: 006 Number of Patents: 008

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
BE 903928	A	19860416	BE 903928	A	19851220		198620 B
GB 2169121	A	19860702	GB 8530130	A	19851206		198627

DE 3543067	A	19860703	DE 3543067	A	19851205	198628
FR 2575312	A	19860627				198632
NL 8503470	A	19860716				198633
GB 2169121	B	19871007				198740
CH 664226	A	19880215				198811
DE 3543067	C	19891116				198946

Priority Applications (No Type Date): CH 846125 A 19841220  
 Language, Pages: BE 903928 (14)

... **uses purchased card whose value is read into meter and then obliterated to prevent re-use**

...Abstract (Basic): A **card** may be introduced into a **reader** which is linked to a control. The control is connected to a credit register and an indicator. In the first of **two** operating **modes**, the control checks the validity of the **card** against stored information and then indicates this on the indicator. The credit register is incremented by one unit, and the equivalent value is obliterated on the **card**.

...

...to the electricity meter and to a cut-out switch, turned on when a valid **card** is introduced, and turned off when the units of credit have been used. The code recorded on a service **card** may be read by the same head as used to read the prepayment **card**.

...Abstract (Equivalent): The electricity meter is installed for cashless payment of electrical energy using prepaid payment **cards** (1) having at least one value unit stored. It has a receiving slot for the **card**, a head (2) for reading stored encoded information on the **card** and another head (3) for erasing a value unit. A control unit (4) is connected...

...4) has a non-volatile memory (8) with a credit register (9) and operates in **two modes**. Validity of the payment **card** (1) is established and shown on a display unit (6) and a credit register (9)...

...amt. The value unit is duly erased. In the second mode, validity of the payment **card** (1) is established and displayed without either incrementing the credit register (9) for erasing any...

...ADVANTAGE - Both payment for electrical energy and checking of payment **card** executed. (5pp)

...Abstract (Equivalent): A collection meter for the cashless supply of electrical power by means of prepaid value **cards** on which at least one unit of value is stored, the meter comprising a receiving slot for receiving a value **card**, a reading head for reading out information stored on the value **card**, a value cancellation head for cancelling the unit of value, and a control means connected...

...first mode of operation to a second mode of operation by insertion of a service **card** into the receiving slot or by actuation of a manually actuatable switch, such that in both modes of operation the validity of the value **card** inserted into the receiving slot is checked on the basis of the information stored thereon...

...Title Terms: **CARD** ;

...Derwent Class: **T05**

...International Patent Class (Additional): **G06K-005/00**

12/3,K/48 (Item 48 from file: 351)

DIALOG(R)File 351:DERWENT WPI

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004573229

WPI Acc No: 86-076573/198612

XRPX Acc No: N86-055993

**Electronic appts. with integral test unit and microprocessor - provides contacts with external access for connection of external electronic test selection unit for different test routines**

Patent Assignee: BOSCH GMBH ROBERT (BOSC )

Inventor: WAZECK J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
DE 3432580	A	19860313	DE 3432580	A	19840905		198612 B

Priority Applications (No Type Date): DE 3432580 A 19840905

Language, Pages: DE 3432580 (5)

**... provides contacts with external access for connection of external electronic test selection unit for different test routines**

...Abstract (Basic): The electronic appts. has **contacts** (18) which are accessible from the outside, and are connected to the plug pins (27...  
...the microprocessor circuit of the electronic appts. can change-over to give a variety of **different test modes** .

...

...from outside to broaden the test range. In addition, in present units where a duty **card** is inserted, the flat type cable can be arranged in the form of a duty **card** . (5pp Dwg.No.1/1)

...Derwent Class: **T01**

**12/3,K/49 (Item 49 from file: 351)**

DIALOG(R)File 351:DERWENT WPI

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003770219

WPI Acc No: 83-766434/198338

XRPX Acc No: N83-165474

**Byte-oriented line adaptor system - is for interfacing remote data terminals to 1-0 sub-system and has receiver transmitted and timer to control baud rate**

Patent Assignee: BURROUGHS CORP (BURS )

Inventor: BIEHL P D; CATILLER R D; LOSKORN R A

Number of Countries: 007 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Main IPC	Week
EP 88618	A	19830914	EP 83301198	A	19830307		198338 B
US 4514824	A	19850430	US 82355185	A	19820305		198520
EP 88618	B	19870708					198727
DE 3372417	G	19870813					198733

Priority Applications (No Type Date): US 82355135 A 19820305; US 82355185 A 19820305

Filing Details:

Patent	Kind	Filing Notes	Application	Patent
EP 88618	A			

Designated States (Regional): BE DE FR GB NL SE

EP 88618 B

Designated States (Regional): BE DE FR GB NL SE

Language, Pages: EP 88618 (E, 41); EP 88618 (E)

...Abstract (Basic): single line adapter format or may be in a multiple

line adapter format on one **card** . Working in conjunction with a state machine processor, the line adapter provides for usage of synchronous mode transmission or asynchronous mode transmission in addition to adaptation for **different** byte-oriented **protocols** .

...Title Terms: **INTERFACE** ;  
Derwent Class: **T01** ;

**12/3,K/50** (Item 50 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
(c) 2000 DERWENT INFO LTD. All rts. reserv.

002342534

WPI Acc No: 80-E8981C/198022

**Computer peripheral interface - has transmission control unit input connected to registers, and outputs taken to input of output register and interrupt commutators**

Patent Assignee: CHALAKHYAN E P (CHAL-I)

Inventor: DZHANDZHUL E L; OGANYAN G A

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
SU 690471	A	19791015						198022 B

Priority Applications (No Type Date): SU 2412069 A 19761013

**Computer peripheral interface -**

...Abstract (Basic): tape data preparation, printout from magnetic tape onto line printer, preparation of tape and punched **cards** etc...

...The **interface** has **two** **modes** of operation: communication with computer and independent operation. During communication with computer input-output is...

...Title Terms: **INTERFACE** ;  
Derwent Class: **T01**

**12/3,K/51** (Item 51 from file: 351)  
DIALOG(R)File 351:DERWENT WPI  
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001666630

WPI Acc No: 77-A3094Y/197702

**Electromagnetic bit detector for data card reader - is designed to use sensor matrix consisting ferromagnetic cores connected together**

Patent Assignee: MINNESOTA MINING CO (MINN )

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat	No	Kind	Date	Main IPC	Week
FR 2303330	A	19761105						197702 B
IT 1029874	B	19790320						197925

Priority Applications (No Type Date): FR 757218 A 19750307

**Electromagnetic bit detector for data card reader -**

...Abstract (Basic): The electromagnetic bit detector, for a data **card reader** , employs a planar matrix (1) of bit sensors, for detecting coded data bits, represented by magnetisation of corresponding zones of a facing data **card** matrix...

...annular core (13, 14) providing an electromagnetic field, for switching the core magnetisation between the **two** stable **modes** . A read-out line also passes through the centre of each core (13, 14) to...

...Title Terms: **CARD** ;

Derwent Class: **T04**

International Patent Class (Additional): **G06K-007/08**

**12/3,K/52 (Item 1 from file: 347)**

DIALOG(R)File 347:JAPIO

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05841534 \*\*Image available\*\*

METHOD AND DEVICE FOR PROCESSING NON-CONTACT MEDIUM AND ENTRANCE/EXIT MANAGING DEVICE

PUB. NO.: 10-124634 [JP 10124634 A]

PUBLISHED: May 15, 1998 (19980515)

INVENTOR(s): SAITO SATORU

APPLICANT(s): OMRON CORP [000294] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 08-299749 [JP 96299749]

FILED: October 23, 1996 (19961023)

INTL CLASS: **G06K-017/00** ; E05B-049/00; G08B-015/00; G08B-025/04; H04B-005/00

ABSTRACT

... a set mode in accordance with the communicating operation pattern of a non-contact medium **card** ) to be operated in a non-contact state...

...SOLUTION: An entrance/exit managing device has a non-contact ID **card** 11 provided with ID information to be used as a **card** key and is constituted so as to execute data communication by non-contact transmission/reception when the **card** 11 enters into the communication area 14 of a non-contact **card** reader 13 arranged in the vicinity of a door 12. A CPU 21 in the **card** reader 13 detects the communication frequency of communicating operation allowing a user to shield the communication area 14 by the same **card** 11 within a fixed time as an operation pattern and determines **various** setting **modes** to be used as **card** keys in each communication frequency. When a **card** is set up to three modes e.g. shielding once by the **card** 11 is an unlocking mode, twice is a locking mode and three times is a...

**12/3,K/53 (Item 2 from file: 347)**

DIALOG(R)File 347:JAPIO

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05828816 \*\*Image available\*\*

NONCONTACT PROCESSOR

PUB. NO.: 10-111916 [JP 10111916 A]

PUBLISHED: April 28, 1998 (19980428)

INVENTOR(s): WAKABAYASHI NAOYUKI

APPLICANT(s): OMRON CORP [000294] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 08-287522 [JP 96287522]

FILED: October 08, 1996 (19961008)

INTL CLASS: **G06K-017/00** ; **G06K-019/07** ; H04B-005/00

ABSTRACT

...SOLUTION: A noncontact **card** A is able to have a bidirectional communication with both a remote type **reader** writer and a nearby type **reader** writer. A CPU 16 drives and controls a power reception part 18, a data reception...

...necessary data in a RAM 22. For the purpose, the ROM 17 is stored with **two** kinds of **protocol**. Further, a power transmission frequency, a modulating and demodulating method for a transmit and a...

12/3,K/54 (Item 3 from file: 347)

DIALOG(R)File 347:JAPIO

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05359590 \*\*Image available\*\*

IC CARD READER /WRITER AND METHOD FOR TRANSMITTING DATA

PUB. NO.: 08-315090 [JP 8315090 A]

PUBLISHED: November 29, 1996 (19961129)

INVENTOR(s): KONDO YOKO

APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 07-123768 [JP 95123768]

FILED: May 23, 1995 (19950523)

IC CARD READER /WRITER AND METHOD FOR TRANSMITTING DATA

INTL CLASS: G06K-017/00 ; H04L-029/06

ABSTRACT

PURPOSE: To provide an IC **card** **reader** /writer and a data transmitting method capable of corresponding to plural IC **cards** having respectively **different** **protocol** types...

...CONSTITUTION: At the time of activating a received IC **card** 3, the IC **card** **reader** /writer 1 discriminates the protocol type of the **card** 3 based upon initial response data transmitted from the **card** 3. When the protocol type of the **card** 3 is the same as that of a host device 2, the **reader** /writer 1 controls the **card** 3 so as to directly transmit/receive data to/from the host 2, and when the protocol type of the **card** 3 is different from that of the host 2, changes the protocol of the **card** 3 and controls data transmission between the host 2 and the **card** 3.

12/3,K/55 (Item 4 from file: 347)

DIALOG(R)File 347:JAPIO

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04908159 \*\*Image available\*\*

RECORDING MEDIUM CARD

PUB. NO.: 07-200759 [JP 7200759 A]

PUBLISHED: August 04, 1995 (19950804)

INVENTOR(s): TAKEMOTO TAKATOSHI

MUTO NOBUYUKI

APPLICANT(s): ACE DENKEN KK [415523] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 05-335816 [JP 93335816]

FILED: December 28, 1993 (19931228)

RECORDING MEDIUM **CARD**

INTL CLASS: **G06K-019/06 ; G06K-017/00**  
...JAPIO KEYWORD:OCR & OMR Optical **Readers** )

ABSTRACT

PURPOSE: To improve service and to make the most of a **card** as a communication tool by writing a cumulative point to be the total of the points set preliminarily according to the **various** kinds of use **modes** of the **card** in a recycle display layer so that the cumulative point may be visualized...

...CONSTITUTION: On the side of the upper half part of the surface of a **card** base 11 constituting a recording medium **card** 10, recycle display layers 20 on which necessary matters can be repeatedly recorded/deleted by ...

... is reversibly changed by heat in the recycle display layer 20 of this recording medium **card** 10, a cumulative point 21 to be the total of the points set preliminarily according to the **various** kinds of use **modes** of **cards** can be repeatedly written and deleted so that the cumulative point may be visualized. For...

... for writing, the necessary matters can visually be recognized and the value added of the **card** itself can be improved.

**12/3,K/56** (Item 5 from file: 347)

DIALOG(R)File 347:JAPIO

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04604349 \*\*Image available\*\*

TRANSMISSION CONTROL SYSTEM

PUB. NO.: 06-276249 [JP 6276249 A]

PUBLISHED: September 30, 1994 (19940930)

INVENTOR(s): IIJIMA YASUO

APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 05-062370 [JP 9362370]

FILED: March 23, 1993 (19930323)

JOURNAL: Section: E, Section No. 1651, Vol. 18, No. 687, Pg. 112, December 26, 1994 (19941226)

INTL CLASS: **H04L-029/06; G06K-017/00**

ABSTRACT

... a transmitting condition used for each protocol when the plural protocols are supported by an **IC card** .

...  
...CONSTITUTION: A **card reader** /writer is connected through a transmission line to the **IC card** , the **IC-card** is activated according to a signal from the **card reader** /writer, and the transfer of data is mutually operated. The **IC card** is equipped with a means which outputs prescribed initial response data after activation, means which controls at least **two protocols** , means which stores protocol decision information for deciding which protocol is used, means which operates when the **IC card** is activated, and updates the protocol deciding information at the time of outputting the initial

12/3,K/57 (Item 6 from file: 347)

DIALOG(R)File 347:JAPIO

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04028810 \*\*Image available\*\*

IC CARD DEVICE

PUB. NO.: 05-020510 [JP 5020510 A]

PUBLISHED: January 29, 1993 (19930129)

INVENTOR(s): FURUTA SHIGERU

TAKEBAYASHI ETSUSHI

APPLICANT(s): MITSUBISHI ELECTRIC CORP [000601] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 03-168077 [JP 91168077]

FILED: July 09, 1991 (19910709)

JOURNAL: Section: P, Section No. 1552, Vol. 17, No. 299, Pg. 32, June 08, 1993 (19930608)

IC CARD DEVICE

INTL CLASS: G06K-017/00

ABSTRACT

PURPOSE: To present an IC card device capable of handling plural kinds of IC card different in communication protocol .

...

...CONSTITUTION: Plural kinds of communication protocol are stored in a microcomputer circuit 12 of a reader /writer 1, and specific information 22 of the adapted protocol is displayed on a prescribed position of an IC card 2; and when the IC card 2 is inserted, an image sensor circuit 18 drives an image sensor head 19 to

12/3,K/58 (Item 7 from file: 347)

DIALOG(R)File 347:JAPIO

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03956091 \*\*Image available\*\*

PORTABLE ELECTRONIC DEVICE

PUB. NO.: 04-321191 [JP 4321191 A]

PUBLISHED: November 11, 1992 (19921111)

INVENTOR(s): IIJIMA YASUO

APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 03-090172 [JP 9190172]

FILED: April 22, 1991 (19910422)

JOURNAL: Section: P, Section No. 1510, Vol. 17, No. 153, Pg. 158, March 25, 1993 (19930325)

INTL CLASS: G06K-019/07 ; B42D-015/10

ABSTRACT

PURPOSE: To always attain the accurate communication of data even to an external device containing different data communication protocols by selecting a communication protocol corresponding to the external device out of those communication protocols...

...CONSTITUTION: A mask ROM 2 of an IC card 1 contains two types of communication protocols A and B. Then the communication of data is carried out to an external device (reader /writer), a CPU 4 outputs the



initial data showing that the 1st and 2nd priorities...

**12/3,K/59 (Item 8 from file: 347)**

DIALOG(R)File 347:JAPIO

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03956089 \*\*Image available\*\*

PORTABLE ELECTRONIC EQUIPMENT

PUB. NO.: 04-321189 [JP 4321189 A]

PUBLISHED: November 11, 1992 (19921111)

INVENTOR(s): IIJIMA YASUO

APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 03-090174 [JP 9190174]

FILED: April 22, 1991 (19910422)

JOURNAL: Section: P, Section No. 1510, Vol. 17, No. 153, Pg. 157,  
March 25, 1993 (19930325)

INTL CLASS: **G06K-019/07** ; B42D-015/10

ABSTRACT

...CONSTITUTION: **Two** types of **protocols** A and B are provided in a mask ROM 2 of an **IC card** 1 for execution of the communication. When the communication is carried out to an external device (**reader** /writer), the initial data is outputted to the external device from a CPU 4 of the **card** 1. The initial data includes the data that prescribes the type of the communication protocol supported by the **card** 1. The external device receives the initial data and confirms the communication protocol to the **card** 1. For instance, the CPU 4 outputs the initial data to show a fact that...

**12/3,K/60 (Item 9 from file: 347)**

DIALOG(R)File 347:JAPIO

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03848082 \*\*Image available\*\*

**IC CARD READER /WRITER**

PUB. NO.: 04-213182 [JP 4213182 A]

PUBLISHED: August 04, 1992 (19920804)

INVENTOR(s): MATSUOKA HIDEAKI

HINO YOSHIHARU

KODERA YUJI

TAKAHASHI TAKEHIRO

APPLICANT(s): HITACHI MAXELL LTD [000581] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 02-406310 [JP 90406310]

FILED: December 06, 1990 (19901206)

JOURNAL: Section: P, Section No. 1455, Vol. 16, No. 559, Pg. 89,  
November 30, 1992 (19921130)

**IC CARD READER /WRITER**

INTL CLASS: **G06K-017/00**

ABSTRACT

PURPOSE: To provide the **IC card reader** /writer to cope with both the **IC card** for one-line formula and the **IC card** for two-lines formula  
...

...CONSTITUTION: The IC card reader /writer holds the one-line formula or two -lines formula communication mode information included in the reset response signals from the IC card , turns on and off the three-state buffer circuits S1, S2 and S3 based on the information and switches the communication mode. The communication mode in the IC card reader /writer can be switched in accordance with the communication mode in the IC card .

12/3,K/61 (Item 10 from file: 347)

DIALOG(R)File 347:JAPIO

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03511786 \*\*Image available\*\*

DISPLAY DEVICE

PUB. NO.: 03-174686 [JP 3174686 A]

PUBLISHED: July 29, 1991 (19910729)

INVENTOR(s): HONDA IKUFUMI

APPLICANT(s): NIPPON CHEMICON CORP [328741] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 01-312280 [JP 89312280]

FILED: December 02, 1989 (19891202)

JOURNAL: Section: P, Section No. 1268, Vol. 15, No. 424, Pg. 163, October 28, 1991 (19911028)

INTL CLASS: G06K-017/00

#### ABSTRACT

PURPOSE: To facilitate the confirmation of each mode of an IC card , and also, to miniaturize an IC card reader /writer by constituting a display means so as to execute a turn-on display in...

...CONSTITUTION: The device has a first mode in which an IC card is not contained in a card holder, a second mode in which the IC card is contained in the card holder and holds in a state that delivery of information can be executed, and a...

... of different colors in accordance with each mode is provided in the vicinity of a card insertion port 15a. That is, the display means is constituted so that a turn-off...

...in different colors in a second mode and a third mode. In such a way, various modes of the IC card can be confirmed easily, and also, an external shape of an IC card reader / writer can be miniaturized.

12/3,K/62 (Item 11 from file: 347)

DIALOG(R)File 347:JAPIO

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03461742 \*\*Image available\*\*

CARD READER

PUB. NO.: 03-124642 [JP 3124642 A]

PUBLISHED: May 28, 1991 (19910528)

INVENTOR(s): AOKI TATSUO

APPLICANT(s): TAMURA ELECTRIC WORKS LTD [350937] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 01-262078 [JP 89262078]

FILED: October 09, 1989 (19891009)

JOURNAL: Section: M, Section No. 1149, Vol. 15, No. 329, Pg. 7, August 21, 1991 (19910821)

**CARD READER**

INTL CLASS: B65H-005/00; **G06K-013/063** ; G07F-007/08; H01L-041/08; H02N-002/00

**ABSTRACT**

PURPOSE: To eliminate the arrangement of a plurality of vibration plates along a **card** carrying passage as well as to make it possible to determine the length of the **card** carrying passage at will by providing a **card** carrying table which is equipped with a vibration plate in a ring shape generating **multi mode** vibration along paired guides...

... frequency voltage with phase difference  $\phi = 90$  deg. is applied to a vibration plate 50, **multi -mode** vibration composed of radial primary vibration and non-axis symmetrical in-place vibration is caused...

... of friction force between the vibration plate 50 and the guides. This thereby allows a **card** carrying table 12 integrally equipped with the vibration plate to be moved along the guides...

... contact with the guides, is also reversed in the rotational direction. By this constitution, the **card** carrying table is thereby moved to the opposite direction.

**12/3,K/63 (Item 12 from file: 347)**

DIALOG(R) File 347:JAPIO

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02633826 \*\*Image available\*\*

**READER /WRITER**

PUB. NO.: 63-250726 [JP 63250726 A]

PUBLISHED: October 18, 1988 (19881018)

INVENTOR(s): SAKAIRI SHIGERU

YAMAUCHI AKIRA

APPLICANT(s): HITACHI MAXELL LTD [000581] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 62-083749 [JP 8783749]

FILED: April 07, 1987 (19870407)

JOURNAL: Section: P, Section No. 827, Vol. 13, No. 64, Pg. 36, February 14, 1989 (19890214)

**READER /WRITER**

INTL CLASS: G06F-003/08; **G06K-017/00**

**ABSTRACT**

PURPOSE: To allow a **reader /writer** to correspond to the **cards** of **various protocol** specifications by reading out information indicating a protocol specification from an **IC card**, selecting a format table and a parameter setting table and converting data or an instruction...

...CONSTITUTION: An **IC card** 10 mounts a recording part 10b recording information indicating the sort of the **IC card** on its surface. When the **card** 10 loaded to a **reader /writer** 1, a **reader** 7 reads out information from the recording part 10b at first. The information is supplied to a CPU 2 and processed to decide the protocol specification of the **IC card**. The CPU 2 selects a format conversion table and a parameter setting table

corresponding to the sort of the **card** 10 from a memory 7 based on the decided result, converts data or an instruction so that they can be fetched from a host computer 9 by the **card** 10 or converts data read out by the **card** 10 so that the data can be inputted by the computer 9.

12/3,K/64 (Item 13 from file: 347)

DIALOG(R)File 347:JAPIO

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01156677 \*\*Image available\*\*

**CARD** PROCESSOR

PUB. NO.: 58-094077 [JP 58094077 A]

PUBLISHED: June 04, 1983 (19830604)

INVENTOR(s): YOSHIDA SHINYA

APPLICANT(s): OMRON TATEISI ELECTRONICS CO [000294] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 56-193249 [JP 81193249]

FILED: November 30, 1981 (19811130)

JOURNAL: Section: P, Section No. 219, Vol. 07, No. 196, Pg. 59, August 26, 1983 (19830826)

**CARD** PROCESSOR

INTL CLASS: G06K-017/00 ; G06F-015/30

#### ABSTRACT

PURPOSE: To perform the processes of various types of **cards** with just a **card** processor and to simplify the constitution of the processor, by providing a function to give the collation to the suitability of **cards** and a function to print the emboss code of the **card** and to issue the slips and switching these **two** functions with a **mode** changeover switch  
...

...CONSTITUTION: A magnetic head 17 of a **card** reader 15 of a **card** processor reads the information recorded on a magnetic stripe surface of a **card** 16, and this read value is fed to a control circuit 18. At the same time, the **card** 16 is sent to an imprinting device 19 to transcribe the emboss code of the **card** to a slip by an imprint roller 20. An input device 2 is connected to the circuit 18, and the secret number of the **card** 16 is supplied by a ten-key of the device 2. In addition, a mode...

...Thus a switch is carried out by the mode of the switch 22 between a **card** collating function for a cash **card** and an imprinter function for a credit **card** . As a result, these two functions are executed with just a **card** processor.